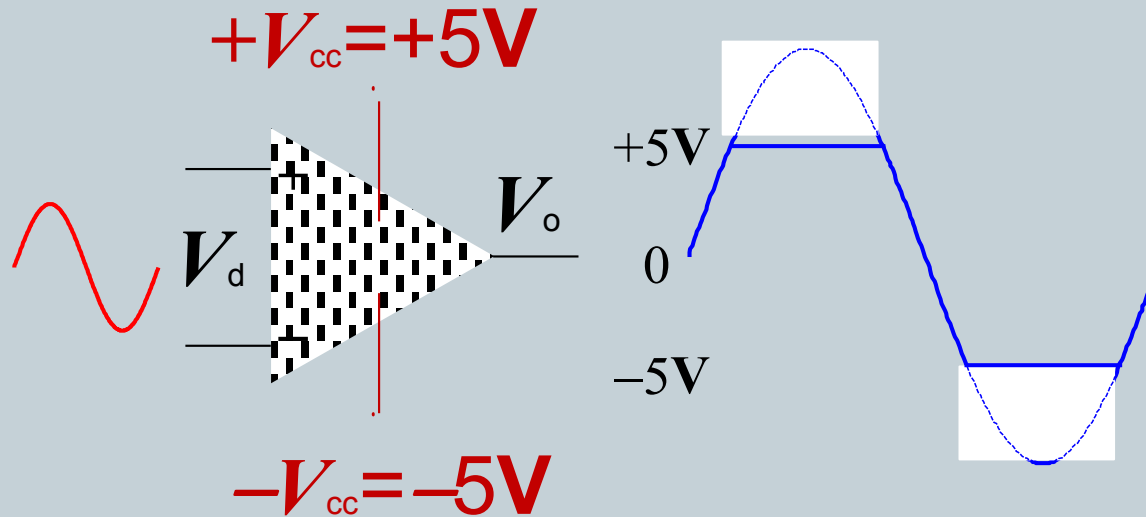


Distortion

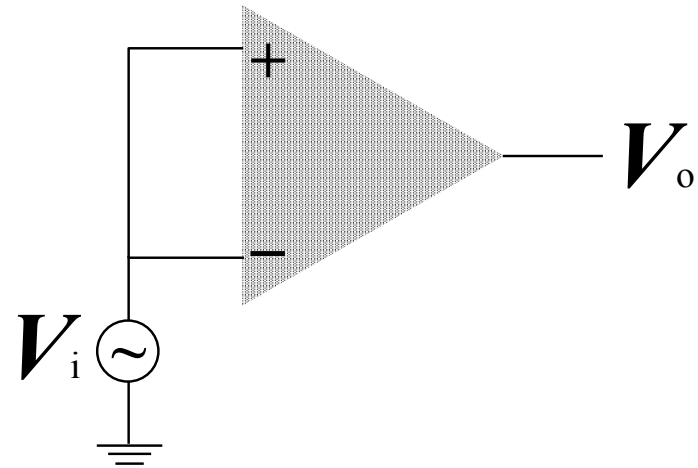
1



The output voltage never exceeds the DC voltage supply of the Op-Amp

Common-Mode Operation

- Same voltage source is applied at both terminals
- Ideally, two input are equally amplified
- Output voltage is ideally zero due to differential voltage is zero
- Practically, a small output signal can still be measured



Note for differential circuits:
Opposite inputs : highly amplified
Common inputs : slightly amplified

⇒ Common-Mode Rejection

Common-Mode Rejection Ratio (CMRR)

Differential voltage input :

$$V_d = V_+ - V_-$$

Common voltage input :

$$V_c = \frac{1}{2}(V_+ + V_-)$$

Output voltage :

$$V_o = G_d V_d + G_c V_c$$

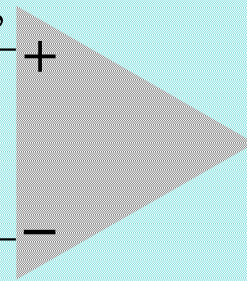
G_d : Differential gain

G_c : Common mode gain

Noninverting

Input

— +



— Output

Inverting

Input

— -

Common-mode rejection ratio:

$$\text{CMRR} = \frac{G_d}{G_c} = 20 \log_{10} \frac{G_d}{G_c} \text{ (dB)}$$

Note:

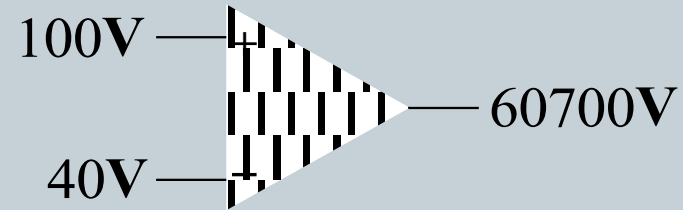
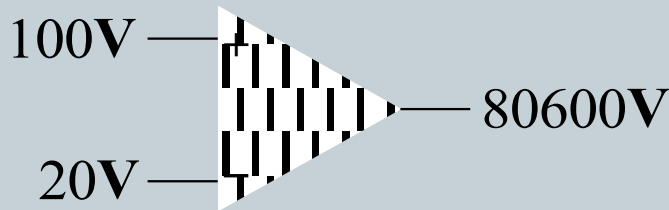
When $G_d \gg G_c$ or $\text{CMRR} \rightarrow \infty$

$\Rightarrow V_o = G_d V_d$

CMRR Example

4

What is the CMRR?



Solution :

$$\left. \begin{aligned} V_{d1} &= 100 - 20 = 80\text{V} \\ V_{c1} &= \frac{100 + 20}{2} = 60\text{V} \end{aligned} \right\} (1)$$

$$\left. \begin{aligned} V_{d2} &= 100 - 40 = 60\text{V} \\ V_{c2} &= \frac{100 + 40}{2} = 70\text{V} \end{aligned} \right\} (2)$$

From (1) $V_o = 80G_d + 60G_c = 80600\text{V}$

From (2) $V_o = 60G_d + 70G_c = 60700\text{V}$

$G_d = 1000$ **and** $G_c = 10 \Rightarrow \text{CMRR} = 20 \log(1000/10) = 40\text{dB}$

NB: This method is Not work! Why?