



SESSION JULY 2016 – DECEMBER 2016

**Electronics & Communication Engineering
Department**



**WORKSHOPS &
FDP**



**INDUSTRIAL
VISITS**



GUEST LECTURES



**SUMMITS &
CONCLAVE**



OTHER EVENTS



WORKSHOPS / FDP

Workshop on Basics of IPR & Commercialization of Technology for Start-Ups and Incubates

- Organized by CII & MSME
- 8th July 2016

Workshop on "Amateur Radio Communications & Computing in Educational Environments"

- Conducted by Dr. Miroslav Skoric, (University of Novi Sad, Serbia)
- 14th & 15th September 2016

ICT Workshop on Preparing Students for Job Interviews

- Conducted by NITTTR Chandigarh
- 9th September & 7th October 2016

One Week Coordinators Workshop on "CMOS, Mixed Signal and Radio Frequency VLSI Design"

- Organized by IIT Kharagpur
- 19th - 23rd September 2016

Workshop on Virtual Lab

- Conducted by IIT Delhi
- 25th October 2016

Workshop on FPGA Based SOC Design

- Organized by IIT Roorkee
- 16th - 24th October 2016

Workshop on Basics of IPR & Commercialization of Technology for Start-Ups and Incubates



A workshop was organized by CII & MSME at MSME-DI, New Delhi on 8th July 2016 pertaining to IPR and Commercialisation of Technology for Start-Ups and Incubatees. Advisor (R&D), Prof. TPN Singh and students of DGI, Greater Noida namely Sachin Kumar Verma, Pankaj Kumar of ECE Department attended the program. Mr. Anjan Das, Executive Director, CII gave the welcome address. He emphasized on the incubation model change wherein industry driven incubation should take place and Corporate sector should drive and guide innovations.

An International workshop on “Amateur Radio Communications & Computing in Educational Environments”



Two days International workshop on “Amateur Radio Communication & Computing in Educational Environment” conducted by Dr. Miroslav Skoric, (University of Novi Sad, Serbia) at ABES Engineering College, Ghaziabad, on 14 & 15 September 2016. The main objective of the workshop was to provide knowledge in the area of Amateur Radio Communication and Amateur Radio Community. Mr. Nitin Kumar, Assistant Professor (ECE) along with 7 ECE students from Dronacharya Group of Institutions, Greater Noida attended the workshop.

ICT Workshop on Preparing Students for Job Interviews



Dronacharya Group of Institutions, Greater Noida in collaboration with NITTTR, Chandigarh conducted the ICT Workshop on “Preparing Students for Job Interviews” under the National Mission on Education through ICT (NMEICT) on 9th September and 7th October 2016. Thirty students from ECE Department attended the Workshop. The event was inaugurated by Dr. PK Tulsi, Head of the Department of Education and Educational Management, NITTTR, Chandigarh.

One Week Coordinators Workshop on “CMOS, Mixed Signal and Radio Frequency VLSI Design”



One Week Coordinator’s Workshop on “CMOS, Mixed Signal and Radio Frequency VLSI Design” under the National Mission on Education through ICT (NMEICT) was organized by IIT Kharagpur during 19th – 23rd September, 2016. Prof. Vinod Kumar, HOD-ECE from Dronacharya Group of Institutions, Greater Noida attended the workshop. Approximately 250 participants from different engineering colleges across the country attended the workshop.

Workshop on Virtual Lab



Department of Electronics and Communication Engineering, Dronacharya Group of Institutions, Greater Noida in association with *IIT Delhi* organized a Workshop on 'Virtual Labs' on 25 October 2016. The objective of the workshop was to create awareness of Virtual Labs among students and faculty members. *Mr. Sajeew Soni, System Analyst VLab Mentor, IIT Delhi*, *Mr. Prateek Sharma, Field Engineer VLab Mentor, IIT Delhi* and *Mr Ashish Ranjan, Field Engineer VLab Mentor, IIT Delhi* were the resource persons for the workshop. Twenty five students of B.Tech 2nd year ECE Department along with the faculty members of all departments attended the workshop.

Workshop on FPGA Based SOC Design



Nine day workshop on "FPGA BASED SOC DESIGN" was organized by IIT, ROORKEE at JOINT ENTRANCE EXAMINATION AND TRAINING, RESEARCH AND DEVELOPMENT CELL UPPER AAMWALA, DEHRADUN (UTTARAKHAND), during 16 Oct. to 24 Oct. 2016. Objective of the workshop was to provide the state-of-art knowledge in the area of Xilinx of the Vivado to learn real world concepts and complement it with unique hands on experience in FPGA Lab kit designed to support all features to realized real time projects and its applications. *Mr. Rajkumar Singh* and *Mr. Satyandra Singh*, Students of ECE VII semester from Dronacharya Group of Institutions, Greater Noida attended the workshop.



INDUSTRIAL VISITS

**Industrial Visit to Advanced Level
Telecom Training Centre (ALTTC),
BSNL, Ghaziabad**

• 26 October 2016

**Industrial Visit to Renewable
Energy India Expo-2016**

• 9 September 2016

Industrial Visit to Advanced Level Telecom Training Centre (ALTTC), BSNL, Ghaziabad



Industrial Visit to Advance Level Telecom Training Centre, BSNL, Ghaziabad was organized by Department of ECE on 26th October 2016 for the students of 2nd & 3rd year of ECE Department of DGI, Greater Noida. Students were accompanied by Mr. Nitin Kumar, Assistant Professor, Department of Electronics & Communication Engineering. The objective of this industrial visit was to make the students aware of the practical techniques used for communication, latest equipment's and components used, and trends in Electronics industry.

Industrial Visit to Renewable Energy India Expo-2016



Renewable Energy India Expo was a 3-day event held from 7th - 9th September 2016 at the India Expo Centre and Mart in Greater Noida, India. This event showcased products from Solar, Wind, Bio-mass/fuel, Small Hydro, Geothermal and Energy Efficiency Sectors in the Power & Energy, Power & Renewable Energy, Solar Energy industries. Mr. Nitin Kumar, Assistant Professor-ECE along with 2nd and 3rd year students of ECE branch from Dronacharya Group of Institution, Greater Noida visited in the expo on 9th September 2016.



Guest Lectures

**Guest Lecture on Network Theory:
Basics & Applications**

• 22 August 2016

**Guest Lecture on Signals &
Systems**

• 1 September 2016

**Guest Lecture on “Introducing
Wavelets transforms through the
Haar Multi resolution Analysis and
connecting it to two band filter
bank”**

• 18 November 2016

Guest Lecture on Network Theory: Basics & Applications



Guest Lecture on “Network Theory: Basics & Applications” was organized by DGI, IEEE Student Branch and Electronics & Communication Engineering Department, *Dronacharya Group of Institutions, Greater Noida* on 22 August 2016. The aim of the lecture was to provide the in-depth knowledge about the Network theory and its applications to the students and faculty members. 48 students of ECE department along with the faculty members of ECE attended the lecture.

Guest Lecture on Signals & Systems



Guest lecture on “*Signals and Systems*” was organized by *IEEE Student Branch and Department of ECE, Dronacharya Group of Institutions Greater Noida* on 1st September 2016 for the students of ECE 2nd, 3rd and 4th year. Prof. (Dr.) S. D Joshi, Head of Department-Electrical Engineering from IIT Delhi was resource person of this lecture. Prof. (Dr.) S D Joshi has rich experience in the field of Digital Signal Processing, Statistical Signal Processing, Image Processing and Group Theoretical Approach to Signal/Image Processing. Faculty members and students of ECE branch of DGI, Greater Noida attended the lecture.

Guest Lecture on “Introducing Wavelets transforms through the Haar Multi resolution Analysis and connecting it to two band filter bank”



Guest Lecture on “Introducing Wavelets transforms through the Haar Multi resolution Analysis and connecting it to two band filter bank” organized by IEEE SSCS Delhi Chapter on 18th Nov. 2016 at ST Microelectronics, Greater Noida. The workshop was led by eminent Professor Dr. V. M. Gadre, EEE Department of IIT Bombay. The lecture provided an insight on Wavelets, Haar Transform and Time Frequency Resolution. The program was attended by Faculty members Ms. Shailika Sharma, Mrs. Rasveen, Mr. Raj Ranjan Prasad from Dept of ECE of DGI, Greater Noida .



SUMMITS & CONCLAVE

**1st Dr. APJ Abdul Kalam
Memorial Lecture on Technology
Trends and Future Research
Directions for India**

- Organized by Confederation of Indian Industries (CII)
- 10th August 2016

**QEEE Session on Classification of
Signals and Systems -
Introduction**

- Organized by IIT Madras
- 1st, 6th and 8th September 2016

**QEEE Session on 'Combinational
and Sequential Circuit Design -
Digital IC Design'**

- Organized by IIT Madras
- 14th, 15th & 16th September 2016

**QEEE Session on Microprocessors
and Microcontrollers - Memory
Mapping and Peripheral
Interfacing**

- Organized by IIT Madras
- 15th, 21st & 22nd September 2016

**Inaugural Ceremony of Smart
India Hackathon 2017**

- Organized by MHRD, Govt. of India
- 9th November 2016

**Dialogue on "Unlocking India's
Digital Potential"**

- Organized by ETV and supported by Ministry of Electronics and Information Technology
- 9th December 2016

1st Dr. APJ Abdul Kalam Memorial Lecture on Technology Trends and Future Research Directions for India



Confederation of Indian Industries (CII) organized 1st Dr. APJ Abdul Kalam Memorial Lecture and Session on Technology Trends and Future Research Directions for India on 10 August 2016 at Taj Mahal Hotel, New Delhi. Prof. TPN Singh, Advisor (R&D) along with student namely Sachin Verma of ECE, Dronacharya Group of Institutions, Greater Noida attended the event.

QEEE Session on Classification of Signals and Systems - Introduction



Quality Enhancement in Engineering Education QEEE Session on 'Classification of Signals & Systems - Introduction' was organized by IIT Madras on 1st, 6th and 8th September 2016 under D2S (Direct to Student) Program. The session was an interactive educational experience through distinct learning modules where live classes through video conferencing were conducted by IIT professors. B.Tech 2nd year ECE students attended the session at Dronacharya Group of Institutions, Greater Noida. Prof. Arun Pachai Kannu (Department of Electrical Engineering, IIT, Mandi) was the resource person in the program.

QEEE Session on *'Combinational and Sequential Circuit Design - Digital IC Design'*



QEEE Session on 'Combinational and Sequential Circuit Design - Digital IC Design' was organized by IIT, Madras under D2S (Direct to Student) Program at Dronacharya Group of Institutions, Greater Noida during 14th - 16th September 2016. The session was scheduled for B. Tech. (ECE) Final Year students. Dr. Nitin Chandrachoodan (IIT, Madras) was the resource person in this program. The program at DGI, Greater Noida was coordinated by Ms. Shailika Sharma (Assistant Professor, Department of ECE).

QEEE Session on *Microprocessors and Microcontrollers - Memory Mapping and Peripheral Interfacing*



QEEE Session on Microprocessors and Microcontrollers - Memory Mapping and Peripheral Interfacing was organized by IIT, Madras under D2S (Direct to Student) Program at Dronacharya Group of Institutions, Greater Noida on 15th, 21st & 22nd September 2016. B. Tech 3rd Year students of ECE Department attended the session at DGI, Greater Noida. The program was conducted by Prof. Ashok Jhunjhunwala, IIT Madras. The program was coordinated by Ms. Rasveen (Assistant Professor, Department of ECE) DGI, Greater Noida.

Inaugural Ceremony of Smart India Hackathon 2017



Union HRD Minister Prakash Javadekar on 9th November 2016 inaugurated the world's largest digital national building initiative "Smart India Hackathon 2017" by handing over a torch to technology students. Prof. Vinod Kumar, HOD-ECE of Dronacharya Group of Institutions, Greater Noida attended the inaugural ceremony.

Prof. Anil Sahasrabudhe Chairman AICTE, Prof. Ved Prakash Chairmen UGC & Mr. R Chandra Shekharan, President NASSCOM also addresses to the technology students to encourage them to think out of the box and participate in this unique initiative.

The aim of this initiative is to harness the creativity and technical expertise of young minds studying in technology institutes to think out of the box and come up with innovative and disruptive tech solutions for some of the daunting problems faced by our nation.

Dialogue on "Unlocking India's Digital Potential"



Dialogue on "Unlocking India's Digital Potential" was organized by ETV and supported by Ministry of Electronics and Information Technology on 9th December, 2016. Purpose of this dialogue was to create the awareness of the Digital Potential of India and bridging the digital divide between urban and rural part of India. Eminent personalities of telecom sectors along with government officials and approximately 100 delegates from different educational institutes participated in this dialogue. Prof. Vinod Kumar, HOD-ECE from Dronacharya Group of Institutions, Greater Noida also attended the session.

Other Events

IOT Project Presentation & Award Ceremony

- Conducted by Consortium for Innovation (COIN)
- 26 August 2016

IOT Project Presentation & Award Ceremony



IOT Project Presentation & Award Ceremony was conducted by Consortium for Innovation (COIN) on 26 August 2016 at DGI Greater Noida. Dr. R. Radhakrishnan, President (COIN) was the chief guest of the program. The COIN organized an 8 weeks Summer Internship Program for Dronacharya Group of Institutions, Greater Noida and Dronacharya College of Engineering, Gurgaon started from 10th June 2016 to complete on 30th July 2016. In this internship program, two teams were selected by Dr. R. Radhakrishnan, President (COIN), one from each institution for the project "IOT Based Healthcare System for the Elderly". After successful completion of Internship program, Students were awarded with certificates and scholarship of Rs 5000 each by Dr. R. Radhakrishnan, President (COIN).