# **Asynchronous Sequential Logic**

#### **LECTURE 5**

## **Reduction of State and Flow Tables**

# **Race-Free State Assignment**

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**Dronacharya Group of Institutions** 

#### **Equivalent States**

Two states are equivalent if for each possible input, they give exactly the same output and go to the same next states or to equivalent next states.

The characteristic of equivalent states is that if (a,b) imply (c,d) and (c,d) imply (a,b), then both pairs of states are equivalent.

### **Implication Table**

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#### **Implied Pairs**





The primary objective in choosing a proper binary state assignment is the prevention of critical races.

Critical races can be avoided by making a binary state assignment in such a way that only one variable changes at any given time when a state transition occurs in the flow table.

b = 01

c = 11

#### **Three-Row Flow-Table Example**



This assignment will cause a critical race during the transition from *a* to *c*.

#### **Three-Row Flow-Table Example**



(a) Flow table

The transition from *a* to *c* must now go through *d*, thus avoiding a critical race.

#### **Multiple-Row Method**

In the multiple-row assignment, each state in the original flow table is replaced by two or more combinations of state variables.



(a) Binary assignment

Note that a2 is adjacent to d2, c1, b2.

### **Multiple-Row Method**





(b) Flow table