

Performance

In DPLL, frequency division number n will affect ϕ_e and Δf_s , so n must be selected properly to let ϕ_e satisfies the system's requirement and let $t_s \downarrow$, $t_c \uparrow$, $\Delta f_s \uparrow$

Frame Synchronization

As mentioned, carrier synchronization and symbol synchronization needs to estimate the phase of synchronous signal which can be realized by using a PLL. Frame synchronization is realized in a different way——inserting frame alignment signal (distinctive bit sequence). Therefore, the basic task of frame synchronization is how to detect the alignment symbol.

Besides add frame alignment bits, some code such as self-synchronizing code can be synchronized without adding extra bits. In this section, we only focus on the first method ——inserting frame alignment signal.

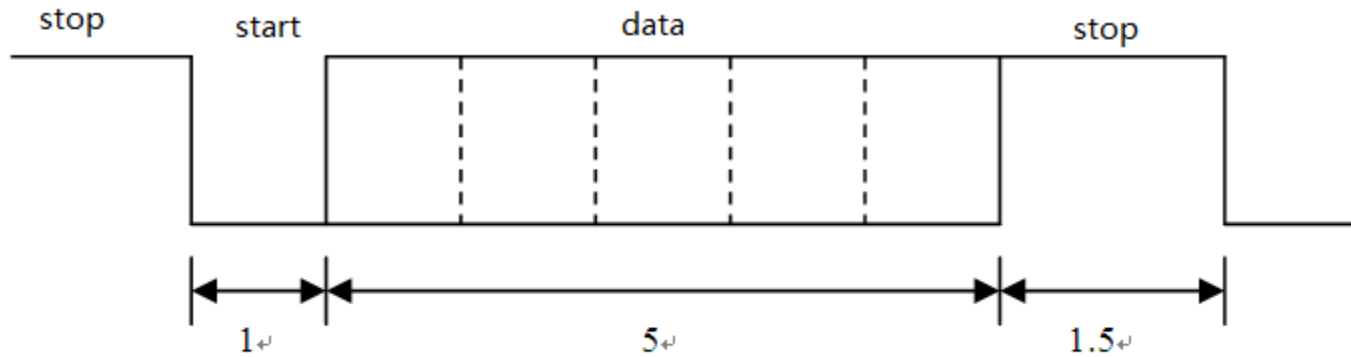
Frame Synchronization

- Bunched frame alignment signal
- Distributed frame alignment signal

Start-stop Method

1. Start-stop method

It is widely used in teleprinter. Each symbol contains 5-8 data bits, a start bit and a stop bit.



Start-stop Method

Drawbacks:

- 1). Low transport efficiency
- 2). Low precision of timing

Bunched frame alignment signal

2. Bunched frame alignment signal

This method inserts synchronous code at a particular place in each frame. The code should have a sharp self-correlation function. The detector should be simple to implement.

Frame synchronization code: Barker code, optimal synchronous code and pseudo-random code.

Barker Code

(1) Barker code:

A n bits barker code $\{x_1, x_2, x_3, \dots, x_n\}$, $x_i = +1$ or -1 . its self-correlation function satisfies:

$$R_x(j) = \sum_{i=1}^{n-j} x_i x_{i+j} = \begin{cases} n & j = 0 \\ 0 \text{ or } \pm 1 & 0 < j < n \\ 0 & j \geq n \end{cases}$$

Barker code is not a periodic sequence. It is proved that when $n < 12100$, we can only find barker code with $n = 2, 3, 4, 5, 7, 11, 13$.

Barker Code

n	barker code
2	++
3	++-
4	+++-, ++-+
5	+++ - +
7	+++ - - + -
11	+++ - - - + - - + -
13	++++ - - ++ - + - +

Barker Code

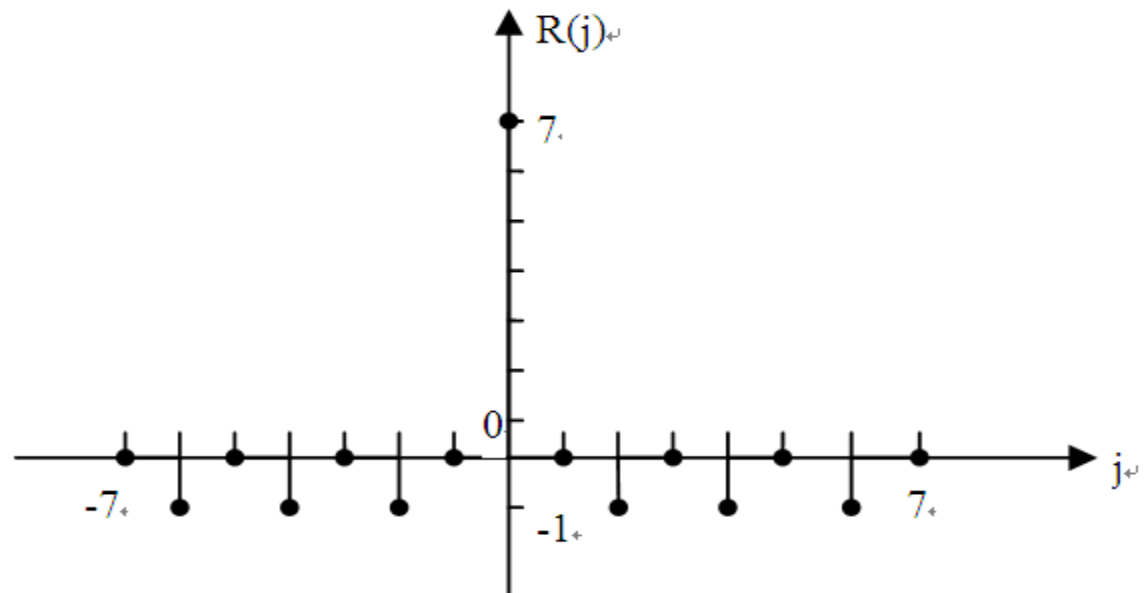
Example: A barker code with $n = 7$, find its self-correlation function

$$j = 0: R_x(0) = x_1x_1 + x_2x_2 + \dots = 7$$

$$j = 1: R_x(1) = x_1x_2 + x_2x_3 + \dots$$

Similarly, we can determine $R_x(j)$.

The result is shown below, we can see it has a sharp peak when $j = 0$.

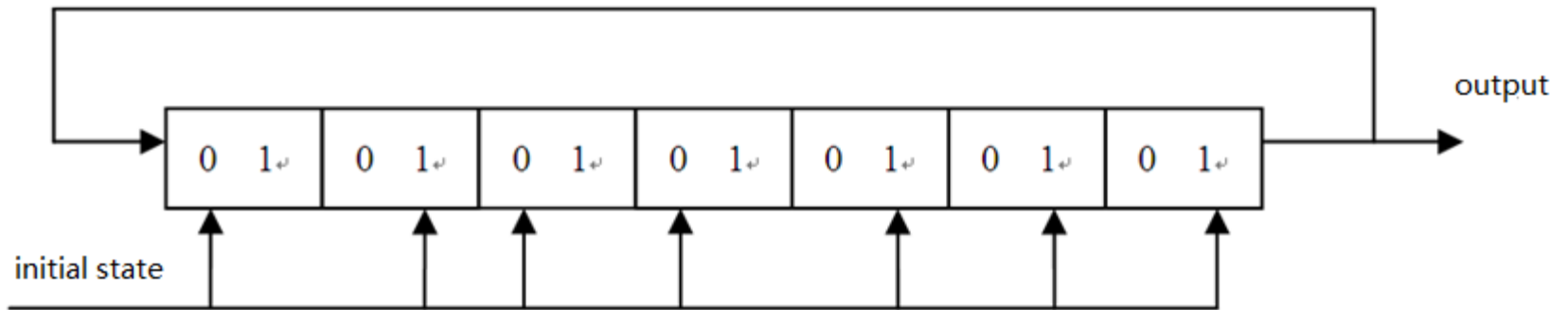


Barker Code

(2) Barker code generator

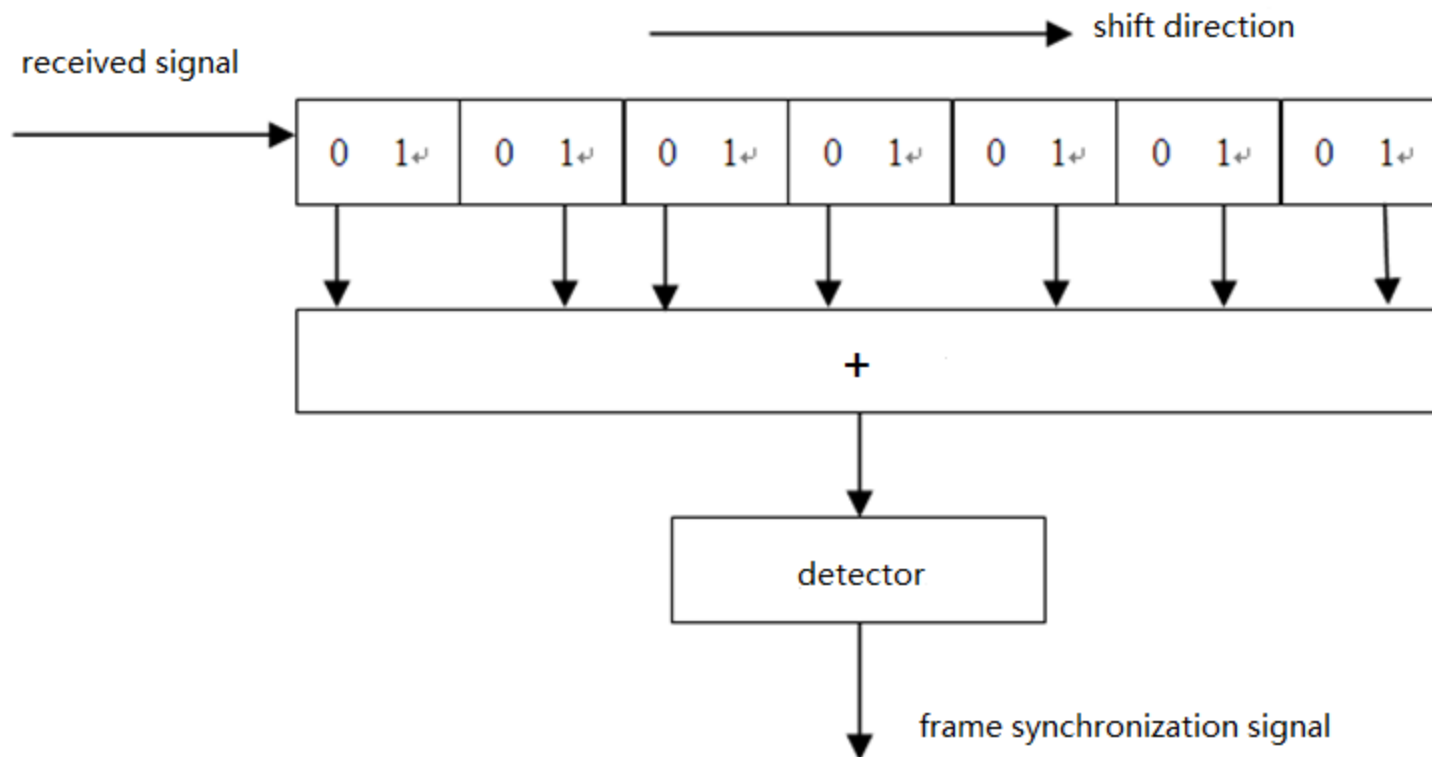
—shift register

Example: when $n=7$, a 7 bits shift register. The initial state is a barker code.



Barker Code

(3) Barker code detector

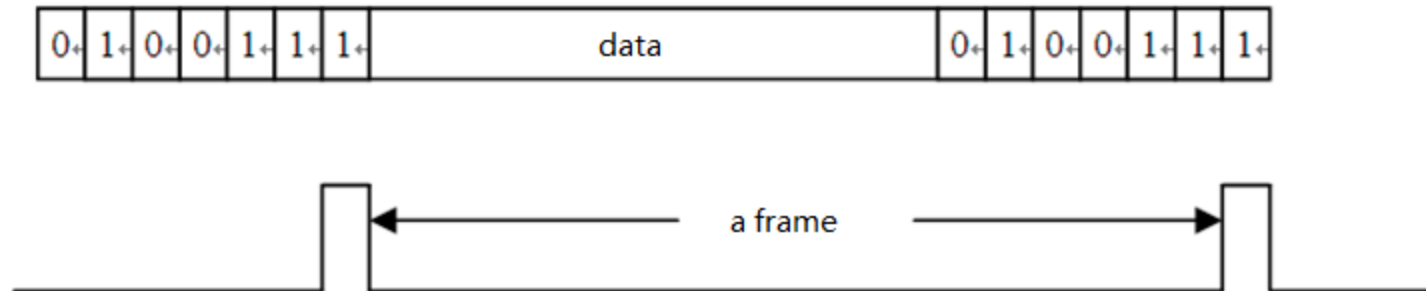


Barker Code

The barker code detector follows:

$$\text{input : "1"} \begin{cases} \text{output "1":?} +1 \\ \text{output "0":?} -1 \end{cases}$$
$$\text{input : "0"} \begin{cases} \text{output "1":?} -1 \\ \text{output "0":?} +1 \end{cases}$$

If the output connection of the shift register is the same with a barker code, then when the input is a barker code, the output of the shift register is "1111111". The detector will send a synchronous impulse.



Distributed frame alignment

signal

3. Distributed frame alignment signal

The synchronous code is distributed in the data signal. That means between each n bits, a synchronous bit is inserted.

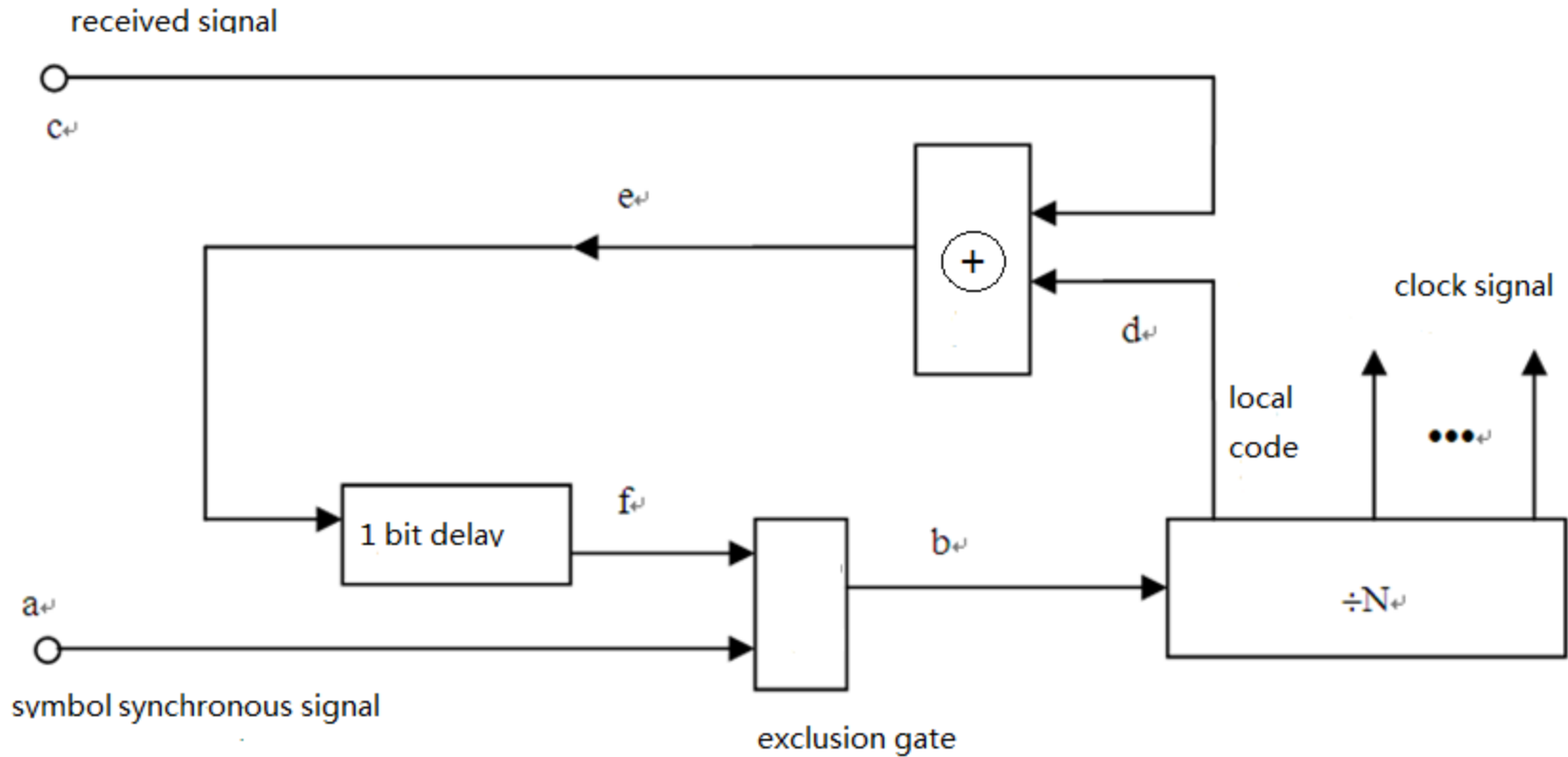
How to design synchronous code:

1. Easy to detect. For example: “11111111” or “10101010”
2. Easy to separate synchronous code and data code. For example: In some digital telephone system, all “0” stands for ring, so synchronous code can only use “10101010”.

To determine the synchronous code, receiver need to detect the code bit by bit. Generally, the code can be detected by shifting the signal code by code.

Distributed frame alignment

signal



Distributed frame alignment signal

Example: data code is all “0”, synchronous code is all “1”

The synchronous code is generated by frequency division ($N=4$) of the symbol synchronous impulse (a). In practical system, the local code (d) will not be exact the same with (c). Therefore, the output of the XOR gate will have nonzero waveform (e). After one bit delay (f), the exclusion gate will discard one symbol synchronous impulse (b). By repeating this procedure, finally (d) and (c) will be exactly the same, frame synchronization is realized.

Performance

Performance of frame synchronization system

—Bunched frame alignment signal

1. Probability of missing synchronization P_L

Affected by noise, the detector may not be able to detect the synchronous code. The probability of this situation is called probability of missing synchronization P_L .

Assume the length of synchronous code is n , bit error rate is P_e . The detector will not be able to detect if more than m bit errors happen, then:

$$P_L = 1 - \sum_{x=0}^m C_n^x P_e^x (1 - P_e)^{n-x}$$

Performance

2. Probability of false synchronization P_F

Since data code can be arbitrary, it may be the same with synchronous code. The probability of this situation is called probability of false synchronization P_F .

P_F equals to the probability of appearance of synchronous code in the data code.

a. In a binary code, assume 0 and 1 appears with the same probability. There are 2^n combinations of a n bit code.

b. Assume when there are more than m bit errors, the data code will also be detected as synchronous code.

When $m = 0$, only $1(C_n^0)$ code will be detected as synchronous code;

When $m = 1$, there are C_n^1 codes will be detected as synchronous code;

.....

Therefore, the probability of false synchronization is:

$$P_F = \frac{\sum_{x=0}^m C_n^x}{2^n} = \left(\frac{1}{2}\right)^n \sum_{x=0}^m C_n^x$$

Performance

P_L and P_F depends on the length of synchronous code n and the maximum bit error m .

When $n \uparrow$, $P_F \downarrow$, $P_L \uparrow$; when $m \uparrow$, $P_L \downarrow$, $P_F \uparrow$

Performance

3. Average build time t_s

Assume both P_L and P_F will not happen, the worst case is we need one frame to build frame synchronization. Assume each frame contains N bits, each bit has a width T_b , then one frame costs NT_b .

Now assume a missing synchronization or a false synchronization also needs NT_b to rebuild the synchronization, then:

$$t_s^1 = NT_b (1 + P_L + P_F)$$

Besides, the average build time of using the distributed frame alignment signal is:

$$t_s^2 = N^2 T_b (N \gg 1)$$

Apparently, $t_s^1 < t_s^2$, so the previous method is more widely used.