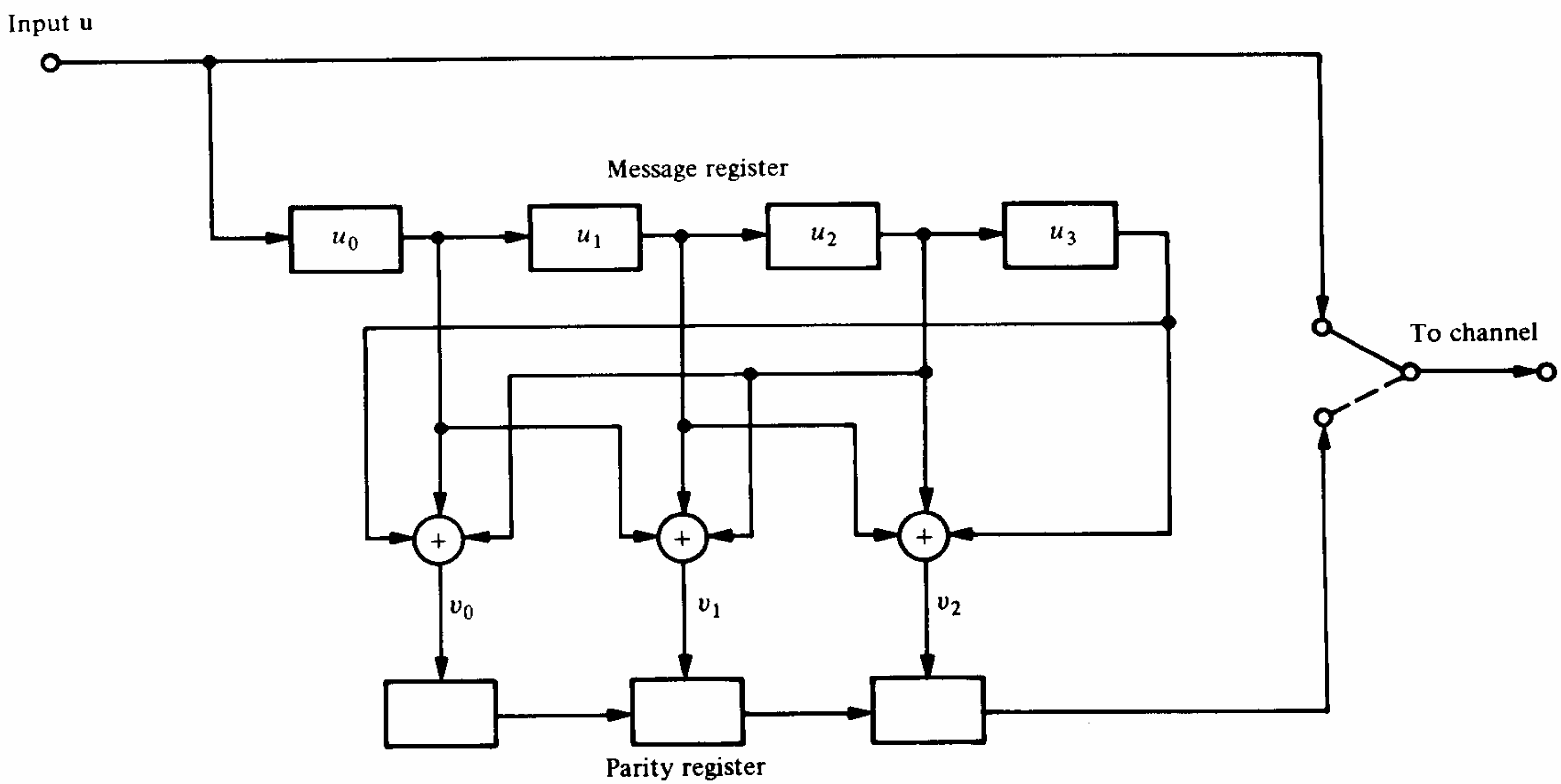
Encoding Circuit



The Encoding Problem (Revisited)

- Linearity makes the encoding problem a lot easier, yet:

  How to construct the G (or H) matrix of a code of minimum distance d_{min}?
- The general answer to this question will be attempted later. For the time being we will state the answer to a class of codes: the Hamming codes.

Hamming Codes

- Hamming codes constitute a class of single-error correcting codes defined as:

  $n = 2^r - 1$, $k = n - r$, $r > 2$
- The minimum distance of the code $d_{min} = 3$
- Hamming codes are perfect codes.
- Construction rule:

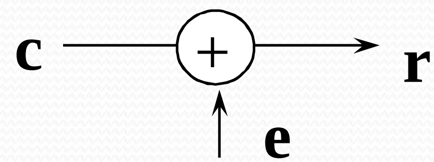
  The H matrix of a Hamming code of order r has as its columns all non-zero r-bit patterns.

  Size of H: $r \times (2^r - 1) = (n-k) \times n$

Decoding

- Let **c** be transmitted and **r** be received, where

$$\mathbf{r} = \mathbf{c} + \mathbf{e}$$



e ≡ error pattern = $e_1 e_2 \ldots\ldots e_n$, where

$$e_i = \begin{cases} 1 & \text{if the error has occured in the } i^{th} \text{ location} \\ 0 & \text{otherwise} \end{cases}$$

The weight of **e** determines the number of errors.

If the error pattern can be determined, decoding can be achieved by:

$$\mathbf{c} = \mathbf{r} + \mathbf{e}$$

Decoding (cont'd)

Consider the (7,4) code.

(1) Let 1101000 be transmitted and 1100000 be received.

Then: **e** = 0001000 (an error in the fourth location)

(2) Let **r** = 1110100. What was transmitted?

	c	**e**
#2	0110100	1000000
#1	1101000	0011100
#3	1011100	0101000

The first scenario is the most probable.

Standard Array

correctable error patterns

$$
\begin{array}{ccccc}
c_0 & c_1 & c_2 & \cdots & c_{2^k-1} \\
e_1 + c_0 & e_1 + c_1 & e_1 + c_2 & \cdots & e_1 + c_{2^k-1} \\
e_2 + c_0 & e_2 + c_1 & e_2 + c_2 & \cdots & e_2 + c_{2^k-1} \\
\vdots & \vdots & \vdots & & \vdots \\
e_{2^{n-k}-1} + c_0 & e_{2^{n-k}-1} + c_1 & e_{2^{n-k}-1} + c_2 & & e_{2^{n-k}-1} + c_{2^k-1}
\end{array}
$$

Standard Array (cont'd)

1. List the 2^k codewords in a row, starting with the all-zero codeword c_0.
2. Select an error pattern $\mathbf{e}_1$ and place it below c_0. This error pattern will be a correctable error pattern, therefore it should be selected such that:

   (i) it has the smallest weight possible (most probable error)

   (ii) it has not appeared before in the array.
3. Repeat step 2 until all the possible error patterns have been accounted for. There will always be $2^n / 2^k = 2^{n-k}$ rows in the array. Each row is called a *coset*. The leading error pattern is the *coset leader*.

Standard Array Decoding

- For an (n,k) linear code, standard array decoding is able to correct exactly 2^{n-k} error patterns, including the all-zero error pattern.
- Illustration 1: The (7,4) Hamming code

  # of correctable error patterns = 2^3 = 8

  # of single-error patterns = 7

  Therefore, all single-error patterns, and only single-error patterns can be corrected. (Recall the Hamming Bound, and the fact that Hamming codes are perfect.

Standard Array Decoding (cont'd)

Illustration 2: The (6,3) code defined by the H matrix:

$$\mathbf{H} = \begin{bmatrix} 1 & 0 & 0 & 0 & 1 & 1 \\ 0 & 1 & 0 & 1 & 0 & 1 \\ 0 & 0 & 1 & 1 & 1 & 0 \end{bmatrix}$$

$$c_1 = c_5 + c_6$$

$$c_2 = c_4 + c_6$$

$$c_3 = c_4 + c_5$$

Codewords

000000
110001
101010
011011
011100
101101
110110
000111

$$d_{\min} = 3$$

Standard Array Decoding (cont'd)

- Can correct all single errors and one double error pattern

000000	110001	101010	011011	011100	101101	110110	000111
000001	110000	101011	011010	011101	101100	110111	000110
000010	110011	101000	011001	011110	101111	110100	000101
000100	110101	101110	011111	011000	101001	110010	000011
001000	111001	100010	010011	010100	100101	111110	001111
010000	100001	111010	001011	001100	111101	100110	010111
100000	010001	001010	111011	111100	001101	010110	100111
100100	010101	001110	111111	111000	001001	010010	100011

The Syndrome

- Huge storage memory (and searching time) is required by standard array decoding.
- Define the syndrome

  $\mathbf{s} = \mathbf{v}\mathbf{H}^T = (\mathbf{c} + \mathbf{e})\,\mathbf{H}^T = \mathbf{e}\mathbf{H}^T$
- The syndrome depends only on the error pattern and not on the transmitted codeword.
- Therefore, each coset in the array is associated with a unique syndrome.

The Syndrom (cont'd)

Error Pattern	Syndrome
0000000	000
1000000	100
0100000	010
0010000	001
0001000	110
0000100	011
0000010	111
0000001	101

Syndrome Decoding

Decoding Procedure:

1. For the received vector **v**, compute the syndrome $\mathbf{s} = \mathbf{v}\mathbf{H}^T$.
2. Using the table, identify the error pattern **e**.
3. Add **e** to **v** to recover the transmitted codeword **c**.

<u>Example</u>:

v = 1110101 ==> **s** = 001 ==> **e** = 0010000

Then, **c** = 1100101

- Syndrome decoding reduces storage memory from $nx2^n$ to $2^{n-k}(2n-k)$. Also, It reduces the searching time considerably.

Decoding of Hamming Codes

- Consider a single-error pattern $\mathbf{e}^{(i)}$, where i is a number determining the position of the error.
- $\mathbf{s} = \mathbf{e}^{(i)}\ \mathbf{H}^{T} = \mathbf{H}_i^{T}$ = the transpose of the i^{th} column of $\mathbf{H}$.
- Example:

$$\begin{bmatrix} 0 & 1 & 0 & 0 & 0 & 0 & 0 \end{bmatrix} \begin{bmatrix} 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \\ 1 & 1 & 0 \\ 0 & 1 & 1 \\ 1 & 1 & 1 \\ 1 & 0 & 1 \end{bmatrix} = \begin{bmatrix} 0 & 1 & 0 \end{bmatrix}$$

Decoding of Hamming Codes (cont'd)

- That is, the (transpose of the) i^{th} column of H is the syndrome corresponding to a single error in the i^{th} position.
- Decoding rule:

1. Compute the syndrome $\mathbf{s} = \mathbf{vH}^T$
2. Locate the error (*i.e.* find i for which $\mathbf{s}^T = \mathbf{H}_i$)
3. Invert the i^{th} bit of $\mathbf{v}$.

Hardware Implementation

- Let $\mathbf{v} = v_0 \ v_1 \ v_2 \ v_3 \ v_4 \ v_5 \ v_6$ and $\mathbf{s} = s_0 \, s_1 \, s_2$
- From the **H** matrix:

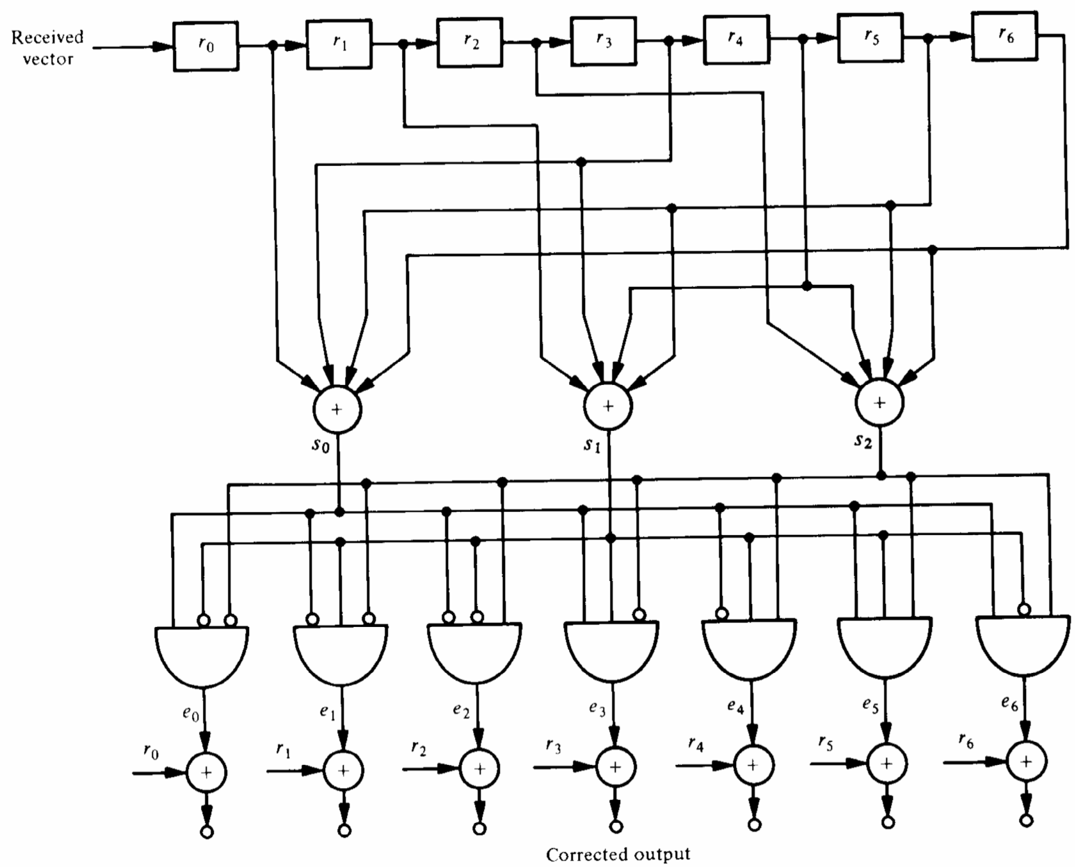
$$s_0 = v_0 + v_3 + v_5 + v_6$$

$$s_1 = v_1 + v_3 + v_4 + v_5$$

$$s_2 = v_2 + v_4 + v_5 + v_6$$

- From the table of syndromes and their corresponding correctable error patterns, a truth table can be construsted. A combinational logic circuit with s_0 , s_1 , s_2 as input and e_0 , e_1 , e_2 , e_3 , e_4 , e_5 , e_6 as outputs can be designed.

Decoding Circuit for the (7,4) HC



v rather than r