

KIRCHOFF'S LAWS

Gustav Robert Kirchhoff (12 March 1824 – 17 October 1887)

was a German physicist who contributed to the fundamental understanding of electrical circuits spectroscopy and the emission of black-body radiation by heated objects.

He also contributed to optics, carefully solving Maxwell's equations to provide a solid foundation for Huygens' principle (and correct it in the process)

CIRCUIT DEFINITIONS

Node – any point where 2 or more circuit elements are connected together

Junction –any point where three or more than three branch meets.

- Wires usually have negligible resistance
- Each node has one voltage (w.r.t. ground)

Branch – a circuit element between two nodes

Loop – a collection of branches that form a closed path returning to the same node without going through any other nodes or branches twice

MESH - A mesh is a loop that has no other loops inside of it.

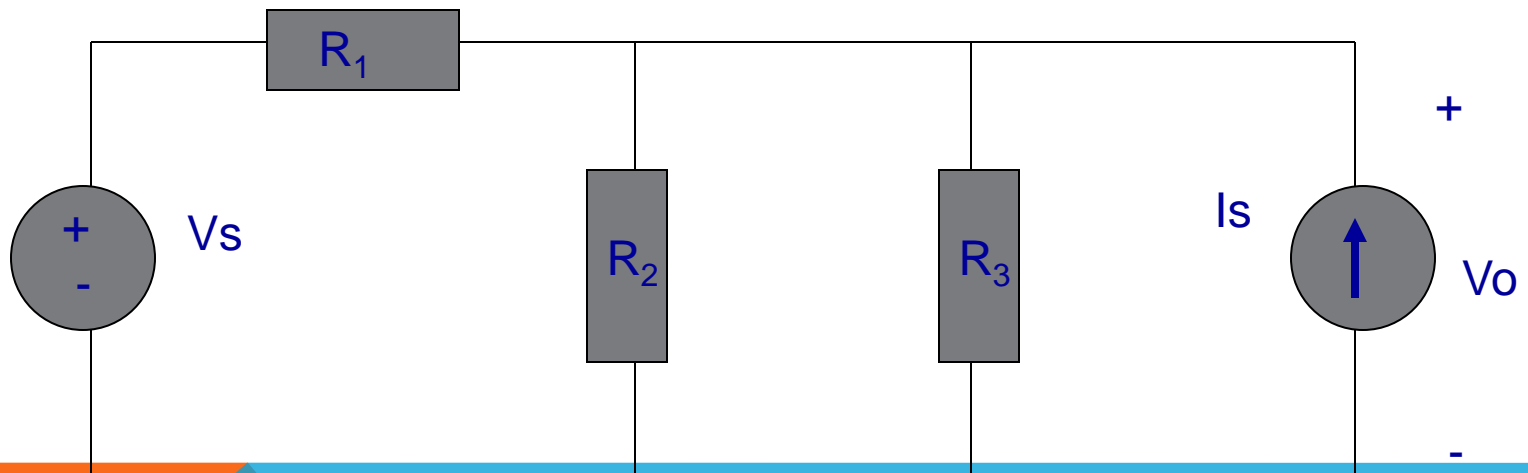
OHM'S LAW

Georg Simon Ohm (**German** 16 March 1789 – 6 July 1854) was a German physicist and mathematician As a school teacher.

Ohm's law states that the current through a conductor between two points is directly proportional to the potential difference across the two points. Introducing the constant of proportionality, the resistance, one arrives at the usual mathematical equation that describes this relationship:

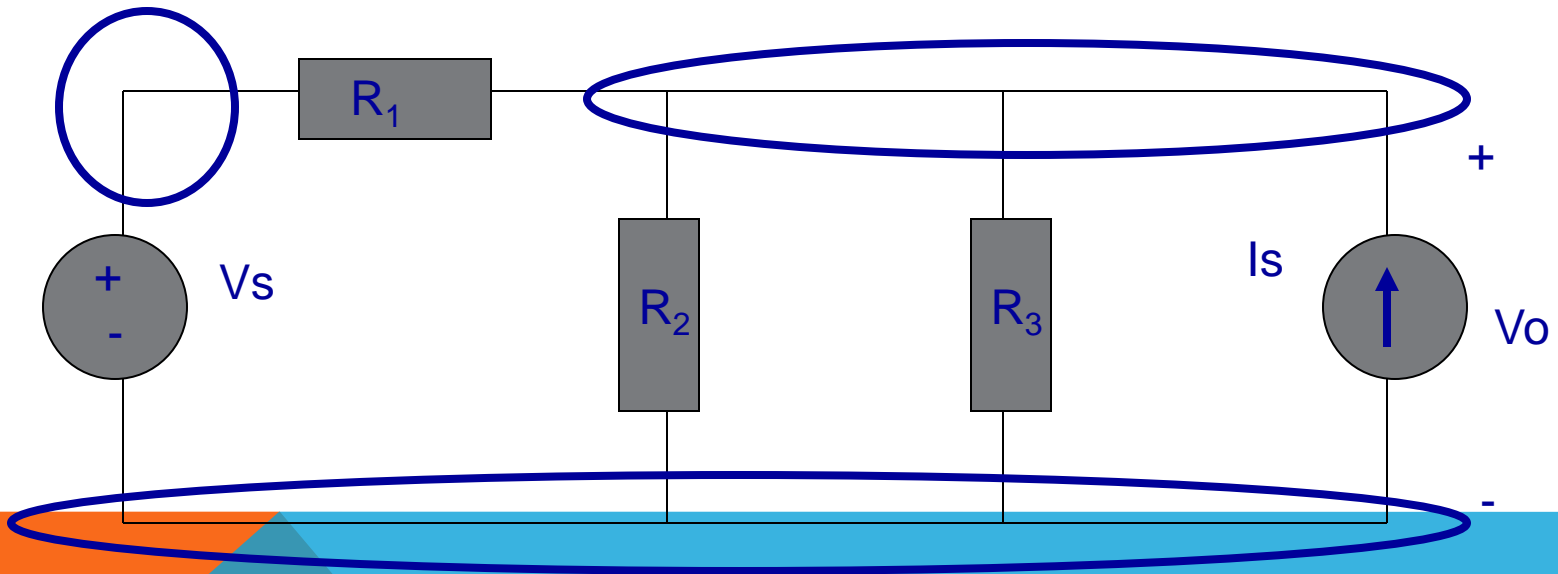
EXAMPLE

How many nodes, branches & loops?



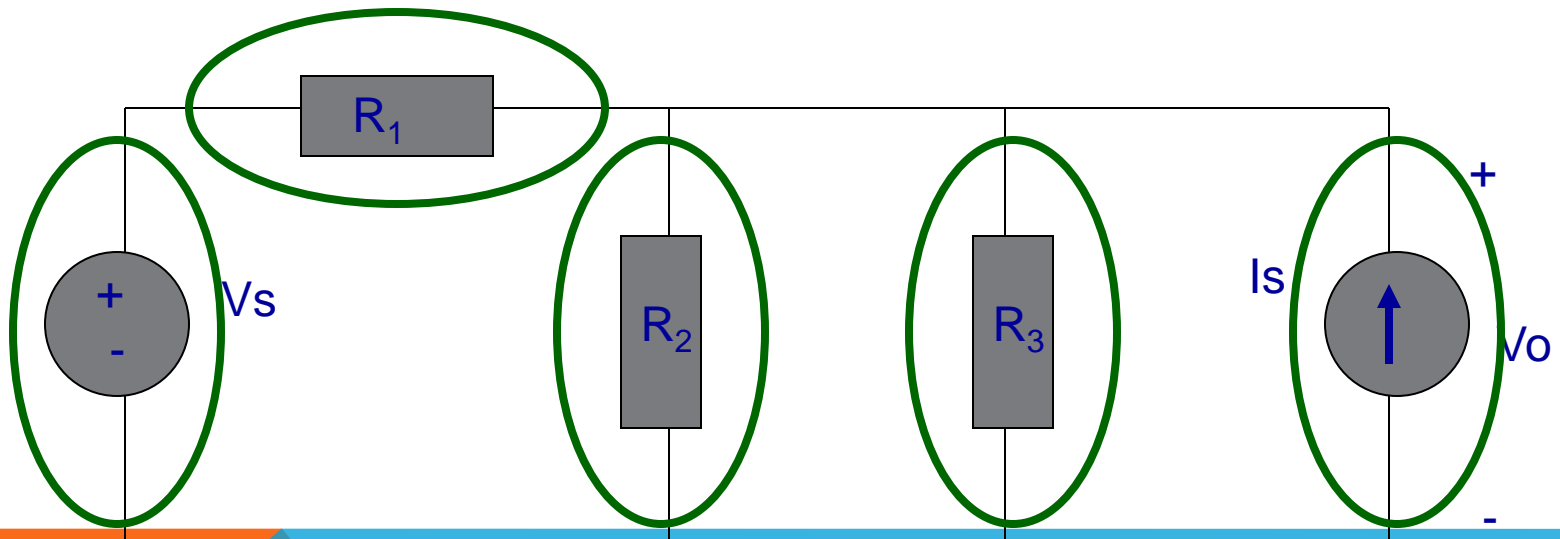
EXAMPLE

Three nodes



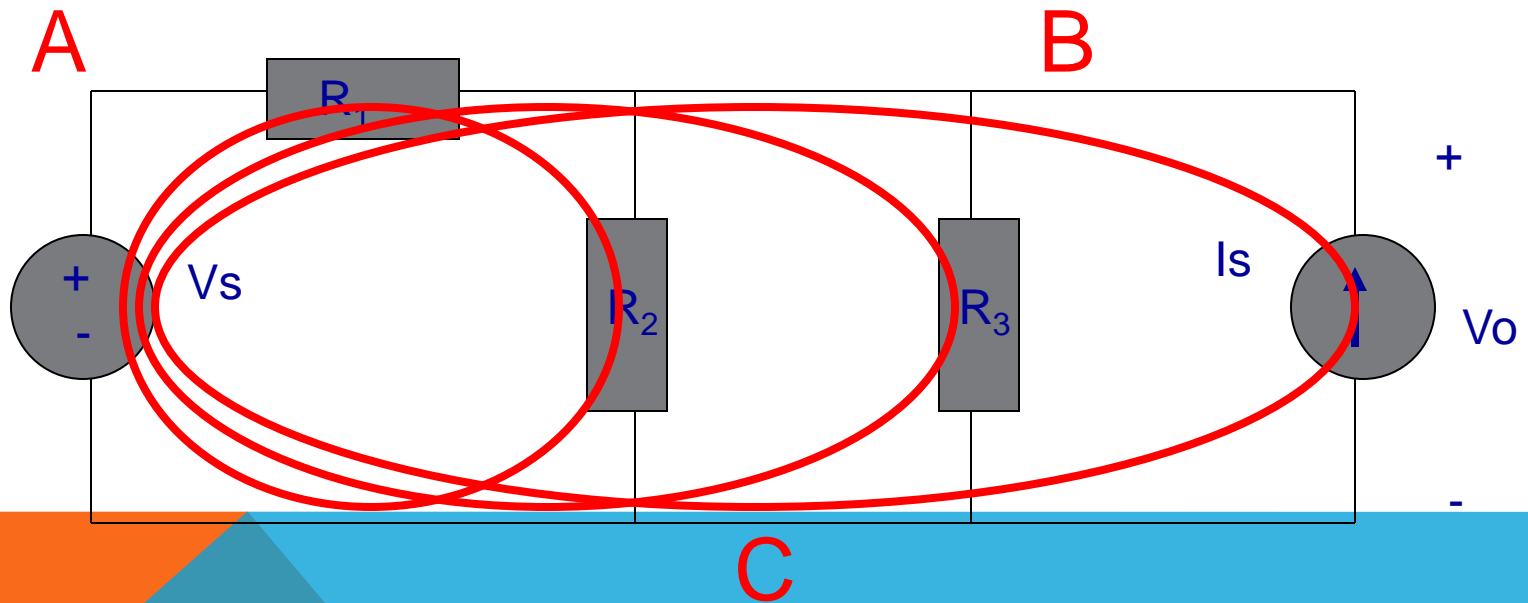
EXAMPLE

5 Branches



EXAMPLE

Five Loops, and three mesh



KIRCHOFF'S VOLTAGE LAW (KVL)

The algebraic sum of voltages around each loop is zero

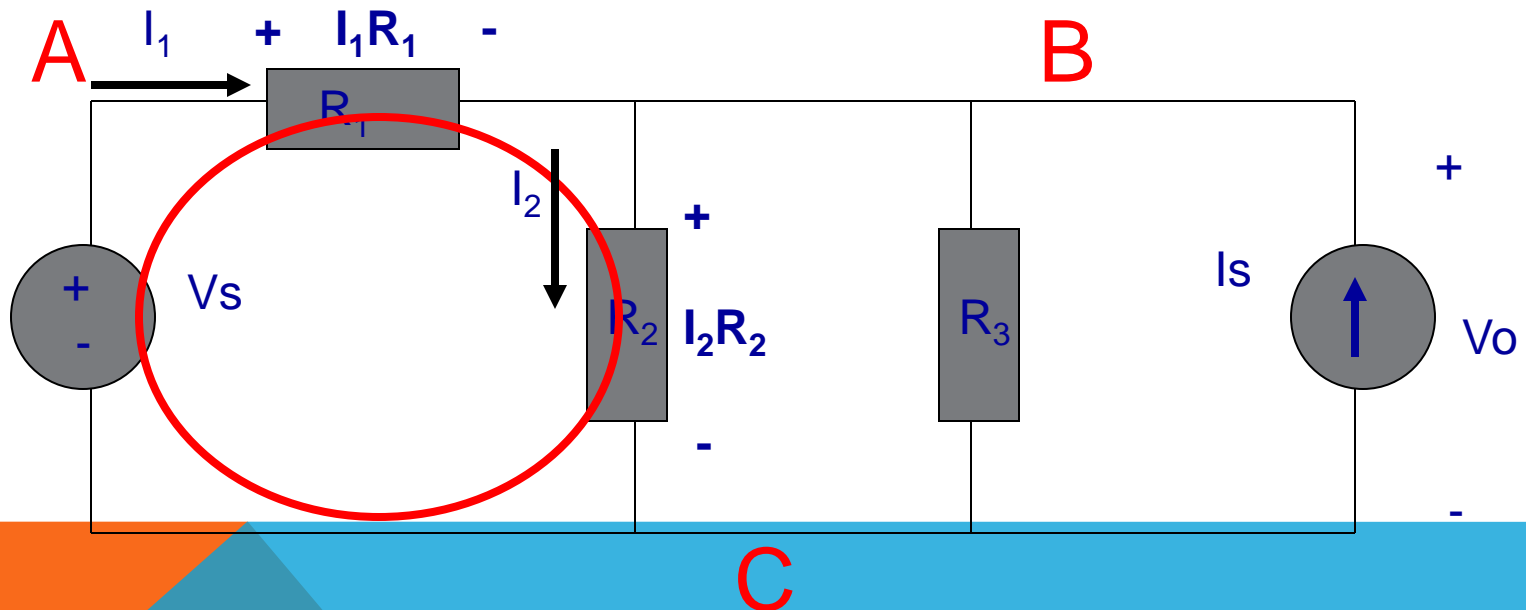
- Beginning with one node, add voltages across each branch in the loop (if you encounter a + sign first) and subtract voltages (if you encounter a – sign first)

Σ voltage drops - Σ voltage rises = 0

Or Σ voltage drops = Σ voltage rises

EXAMPLE

Kirchoff's Voltage Law around 1st Loop

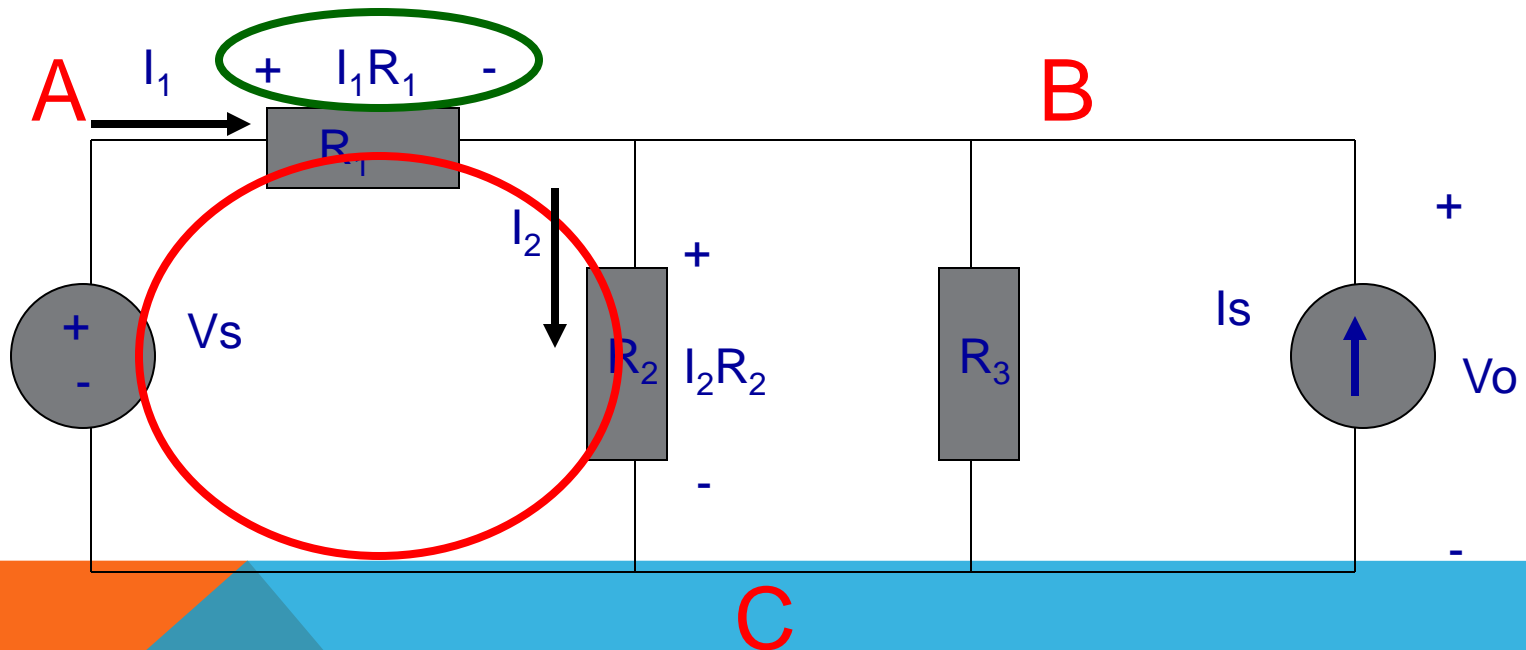


Assign current variables and directions

Use Ohm's law to assign voltages and polarities consistent with passive devices (current enters at the + side)

EXAMPLE

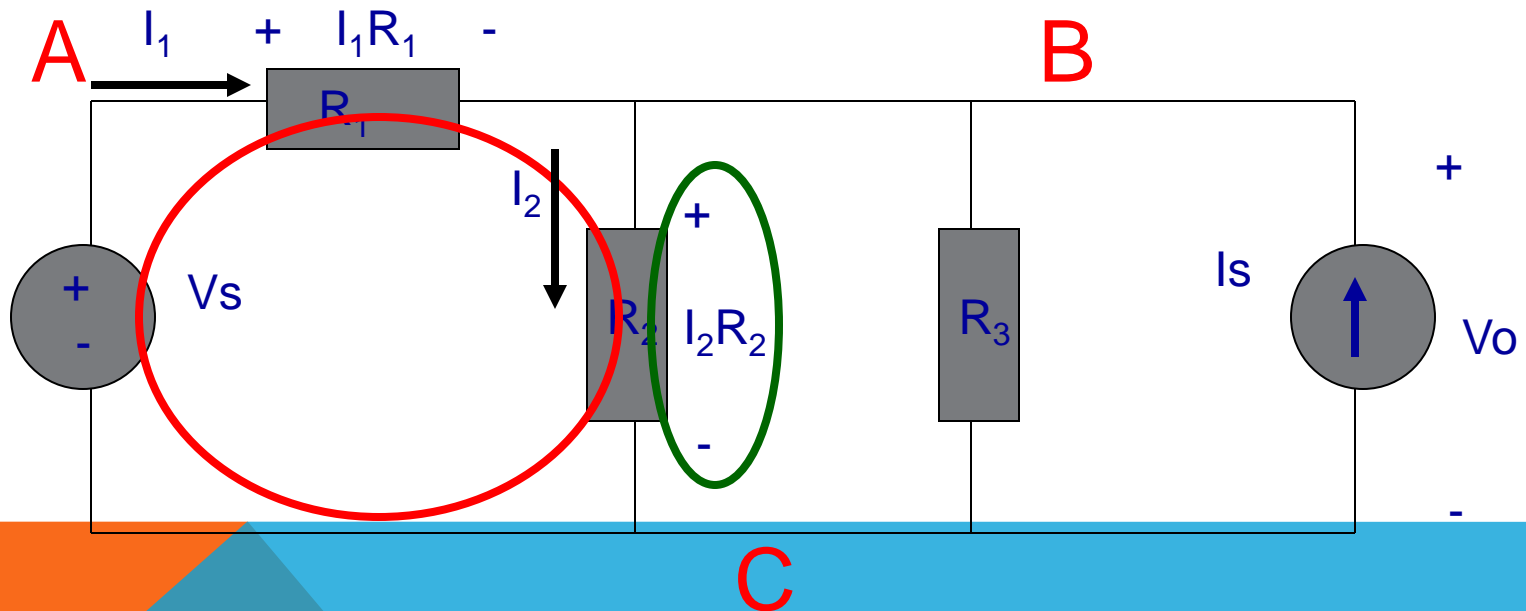
Kirchoff's Voltage Law around 1st Loop



Starting at node A, add the 1st voltage drop: $+ I_1 R_1$

EXAMPLE

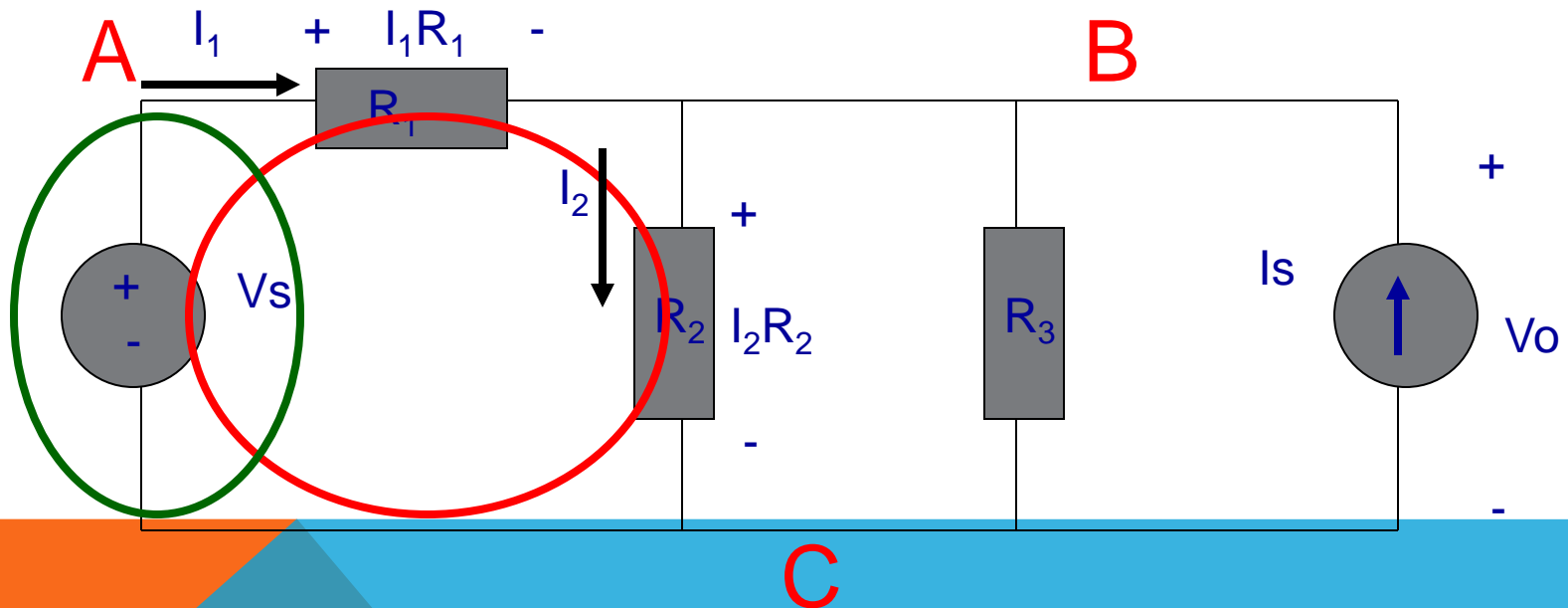
Kirchoff's Voltage Law around 1st Loop



Add the voltage drop from B to C through R_2 : $+ I_1 R_1 + I_2 R_2$

EXAMPLE

Kirchoff's Voltage Law around 1st Loop

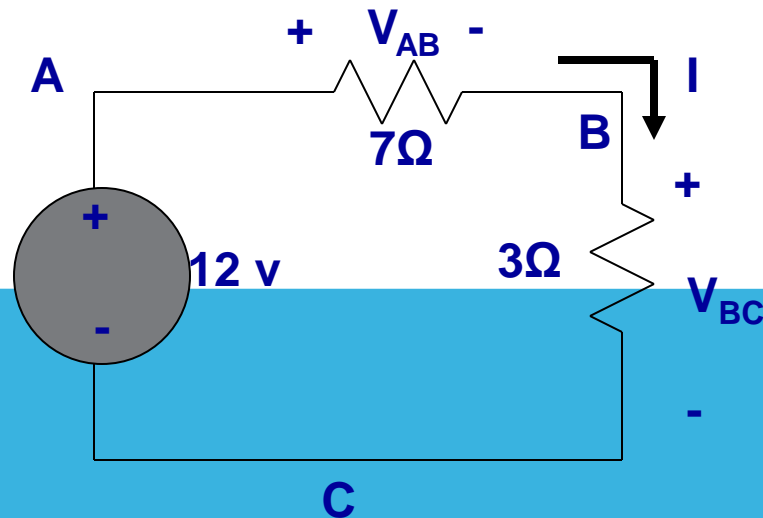


Subtract the voltage rise from C to A through V_s : $+ I_1 R_1 + I_2 R_2 - V_s = 0$

Notice that the sign of each term matches the polarity encountered 1st

CIRCUIT ANALYSIS

When given a circuit with sources and resistors having fixed values, you can use Kirchoff's two laws and Ohm's law to determine all branch voltages and currents



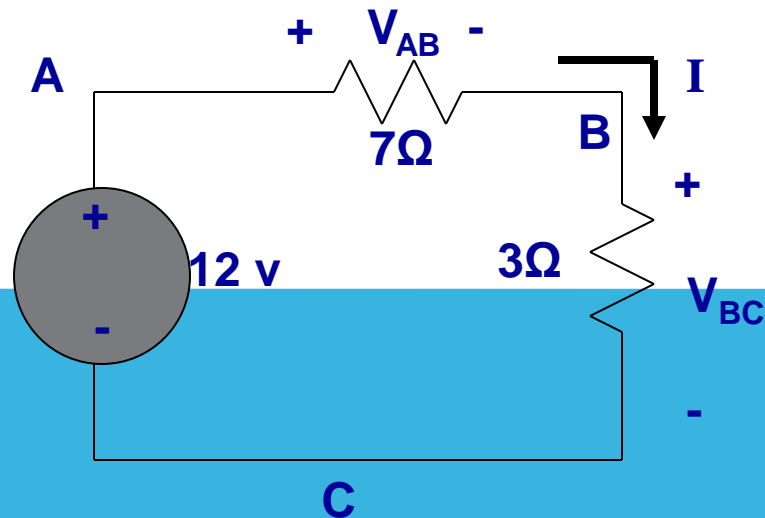
CIRCUIT ANALYSIS

By Ohm's law: $V_{AB} = I \cdot 7\Omega$ and $V_{BC} = I \cdot 3\Omega$

By KVL: $V_{AB} + V_{BC} - 12\text{ v} = 0$

Substituting: $I \cdot 7\Omega + I \cdot 3\Omega - 12\text{ v} = 0$

Solving: $I = 1.2\text{ A}$

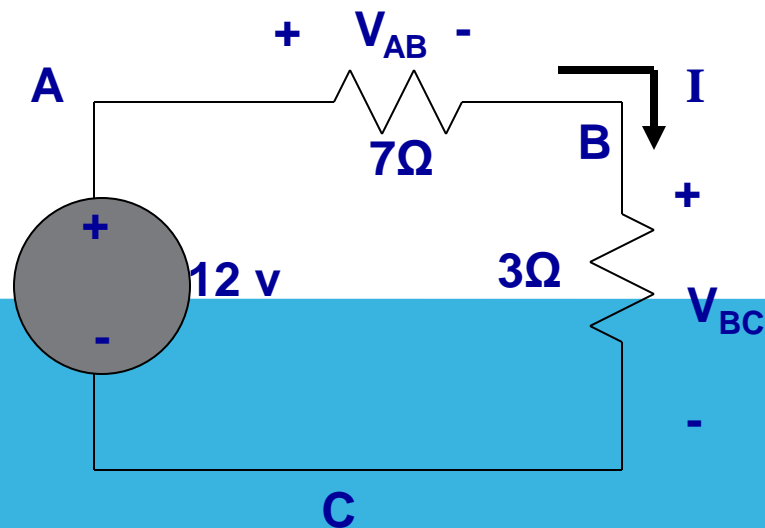


CIRCUIT ANALYSIS

Since $V_{AB} = I \cdot 7\Omega$ and $V_{BC} = I \cdot 3\Omega$

And $I = 1.2 \text{ A}$

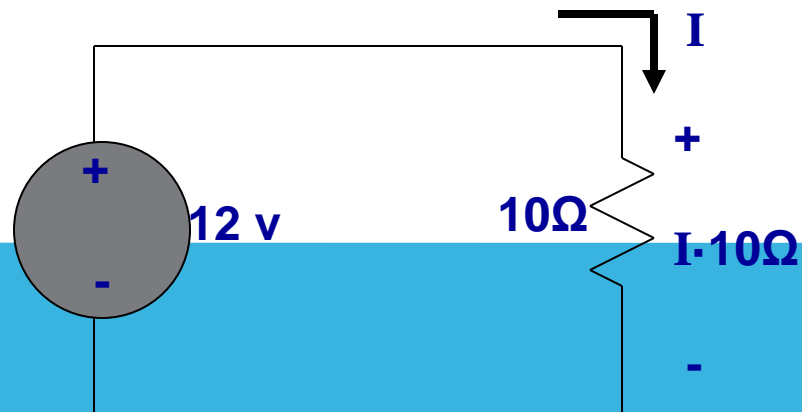
So $V_{AB} = 8.4 \text{ v}$ and $V_{BC} = 3.6 \text{ v}$



SERIES RESISTORS

KVL: $+I \cdot 10\Omega - 12\text{ v} = 0$, So $I = 1.2\text{ A}$

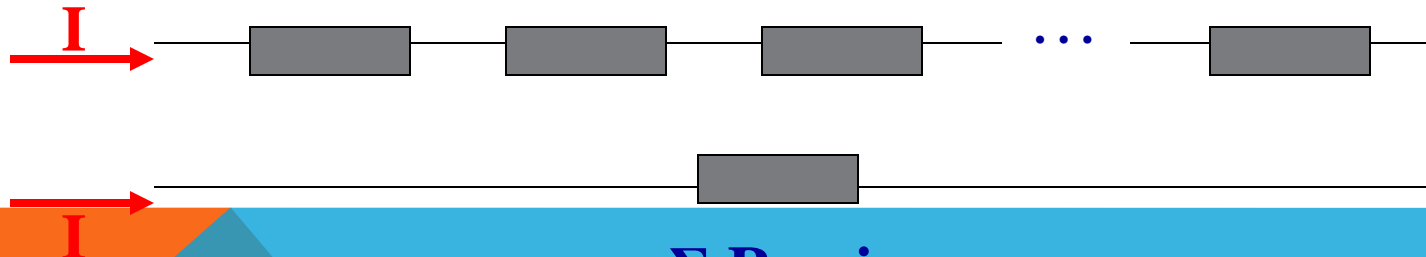
From the viewpoint of the source, the 7 and 3 ohm resistors in series are equivalent to the 10 ohms



SERIES RESISTORS

To the rest of the circuit, series resistors can be replaced by an equivalent resistance equal to the sum of all resistors

Series resistors (same current through all)



ΣR_{series}

KIRCHOFF'S CURRENT LAW (KCL)

The algebraic sum of currents entering a node is zero

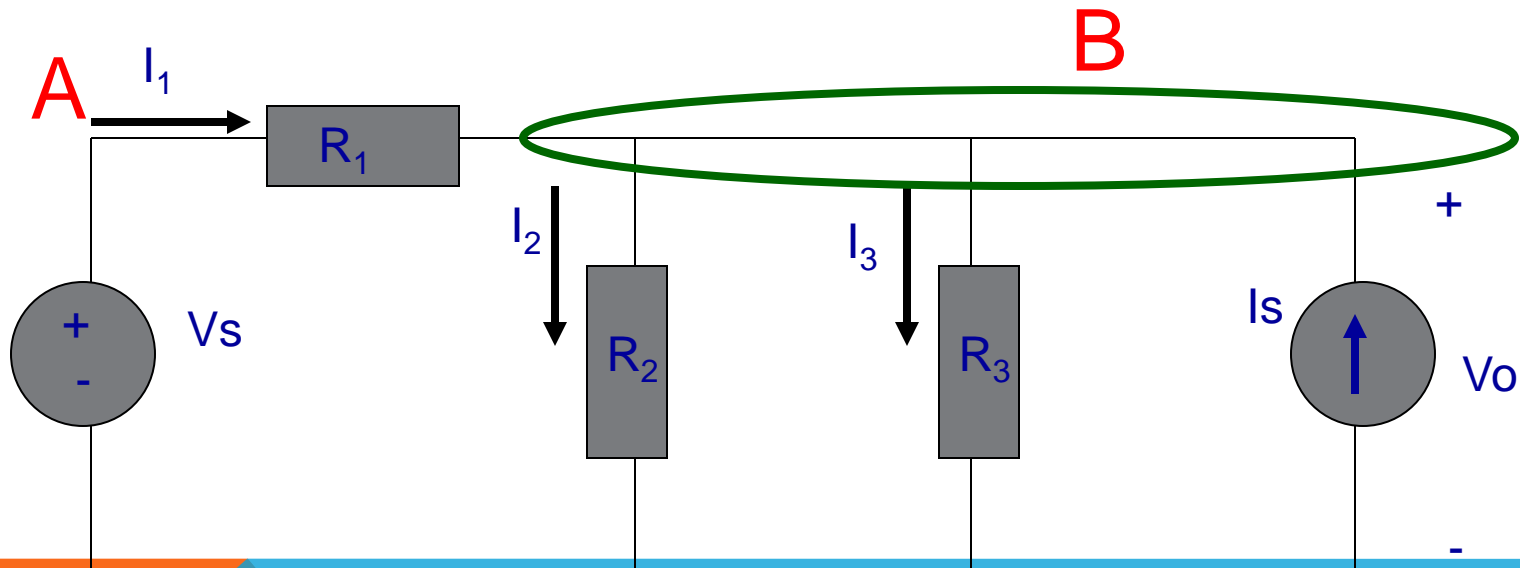
- Add each branch current entering the node and subtract each branch current leaving the node

$$\Sigma \text{ currents in} - \Sigma \text{ currents out} = 0$$

$$\text{Or } \Sigma \text{ currents in} = \Sigma \text{ currents out}$$

EXAMPLE

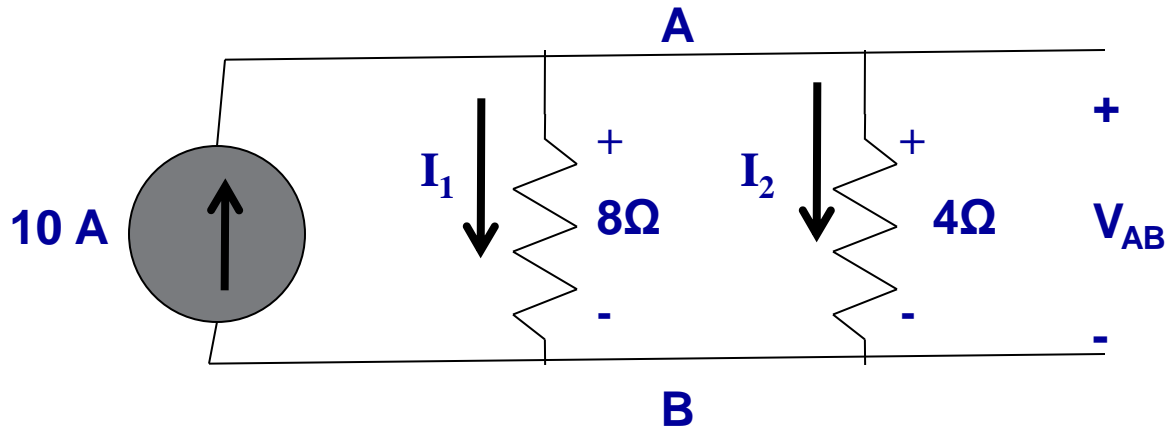
Kirchoff's Current Law at B



Assign current variables and directions

Add currents in, subtract currents out: $I_1 - I_2 - I_3 + I_s = 0$

CIRCUIT ANALYSIS



By KVL: $- I_1 \cdot 8\Omega + I_2 \cdot 4\Omega = 0$

Solving: $I_2 = 2 \cdot I_1$

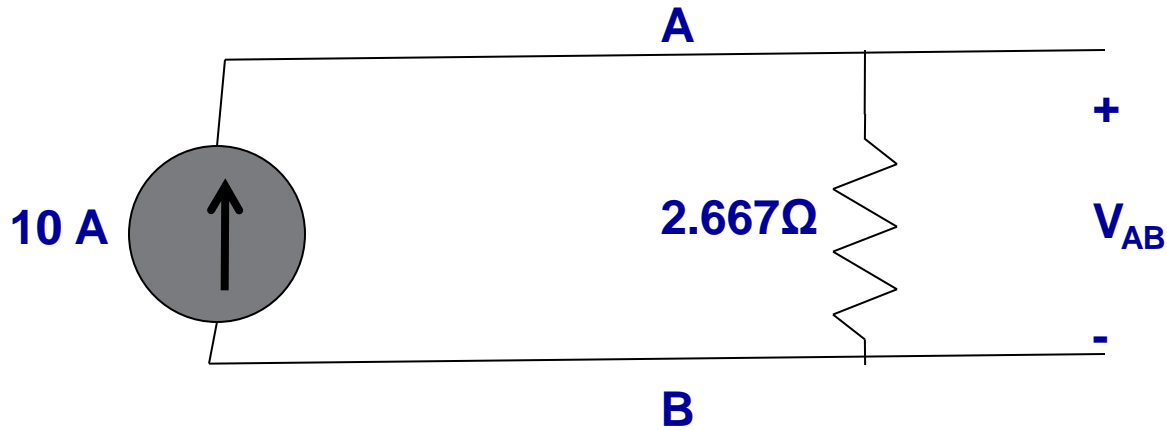
By KCL: $10A = I_1 + I_2$

Substituting: $10A = I_1 + 2 \cdot I_1 = 3 \cdot I_1$

So $I_1 = 3.33 A$ and $I_2 = 6.67 A$

And $V_{AB} = 26.33$ volts

CIRCUIT ANALYSIS



By Ohm's Law: $V_{AB} = 10 \text{ A} \cdot 2.667 \text{ } \Omega$

So $V_{AB} = 26.67 \text{ volts}$

Replacing two parallel resistors (8 and 4 Ω) by one equivalent one produces the same result from the viewpoint of the rest of the circuit.

PARALLEL RESISTORS

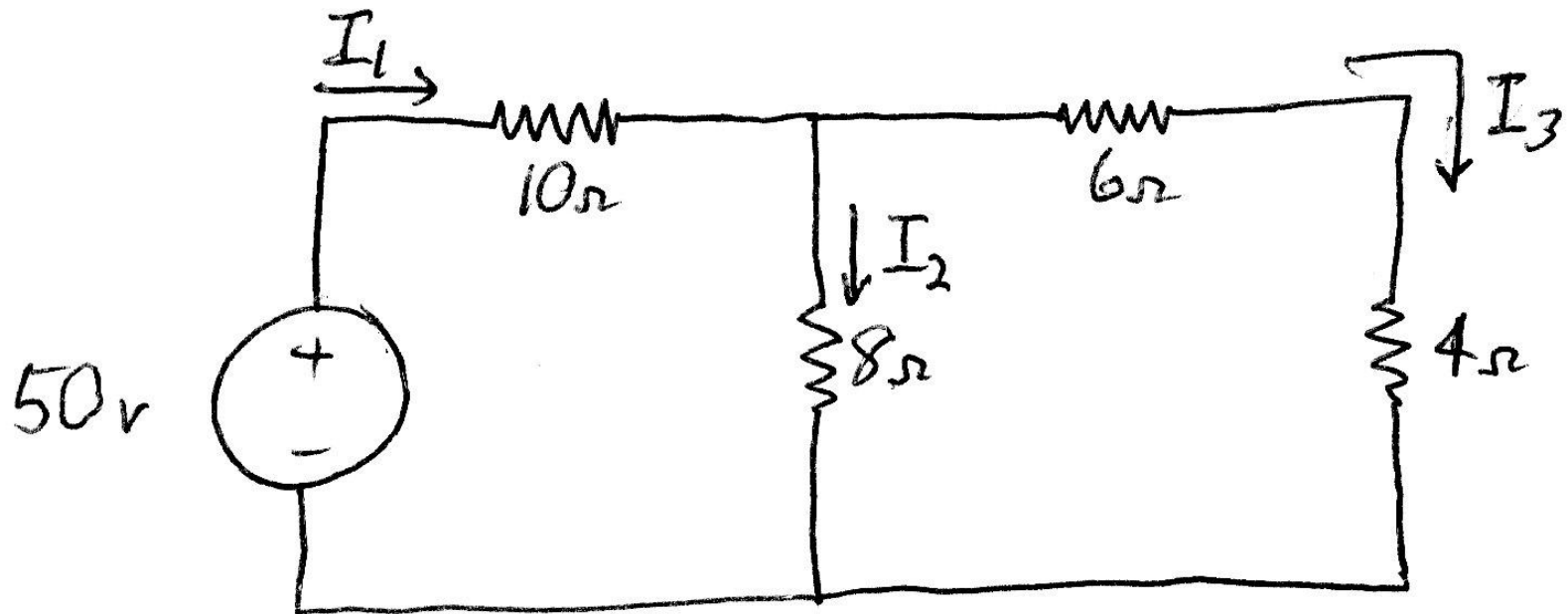
The equivalent resistance for any number of resistors in parallel (i.e. they have the same voltage across each resistor):

$$R_{eq} = \frac{1}{\frac{1}{R_1} + \frac{1}{R_2} + \dots + \frac{1}{R_N}}$$

For two parallel resistors:

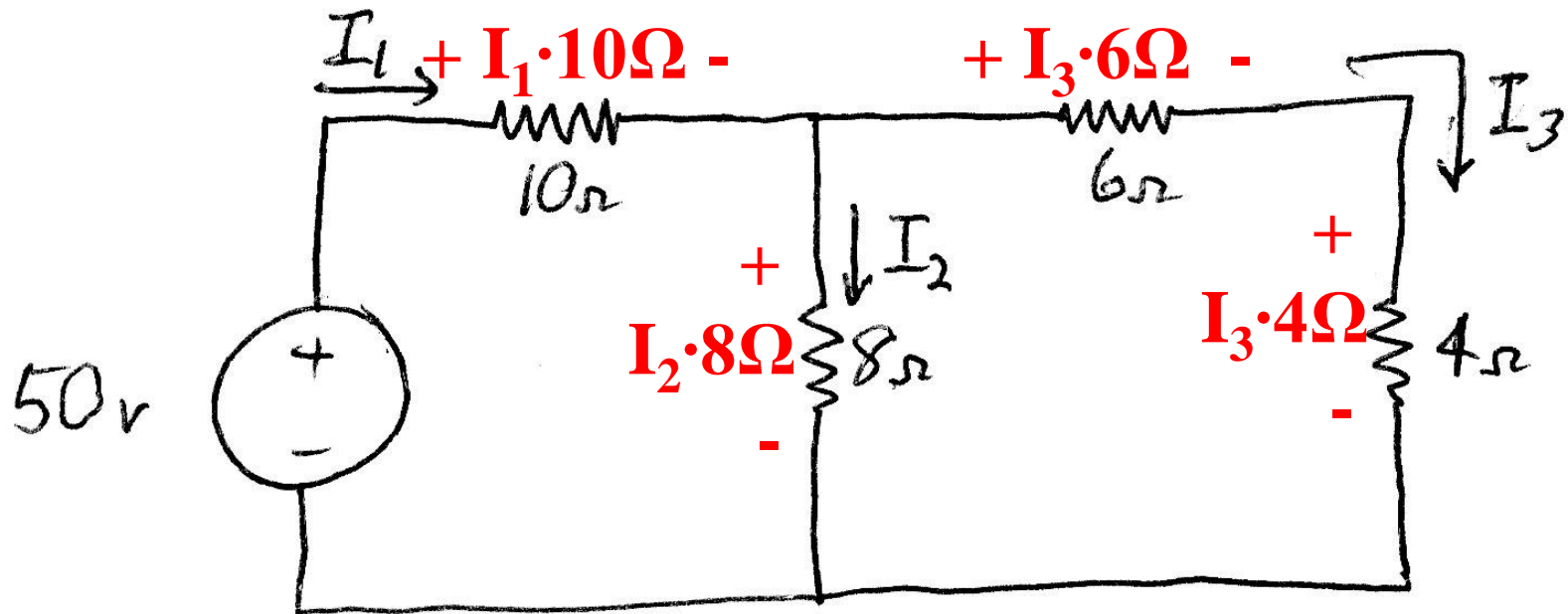
$$R_{eq} = \frac{R_1 \cdot R_2}{R_1 + R_2}$$

EXAMPLE CIRCUIT



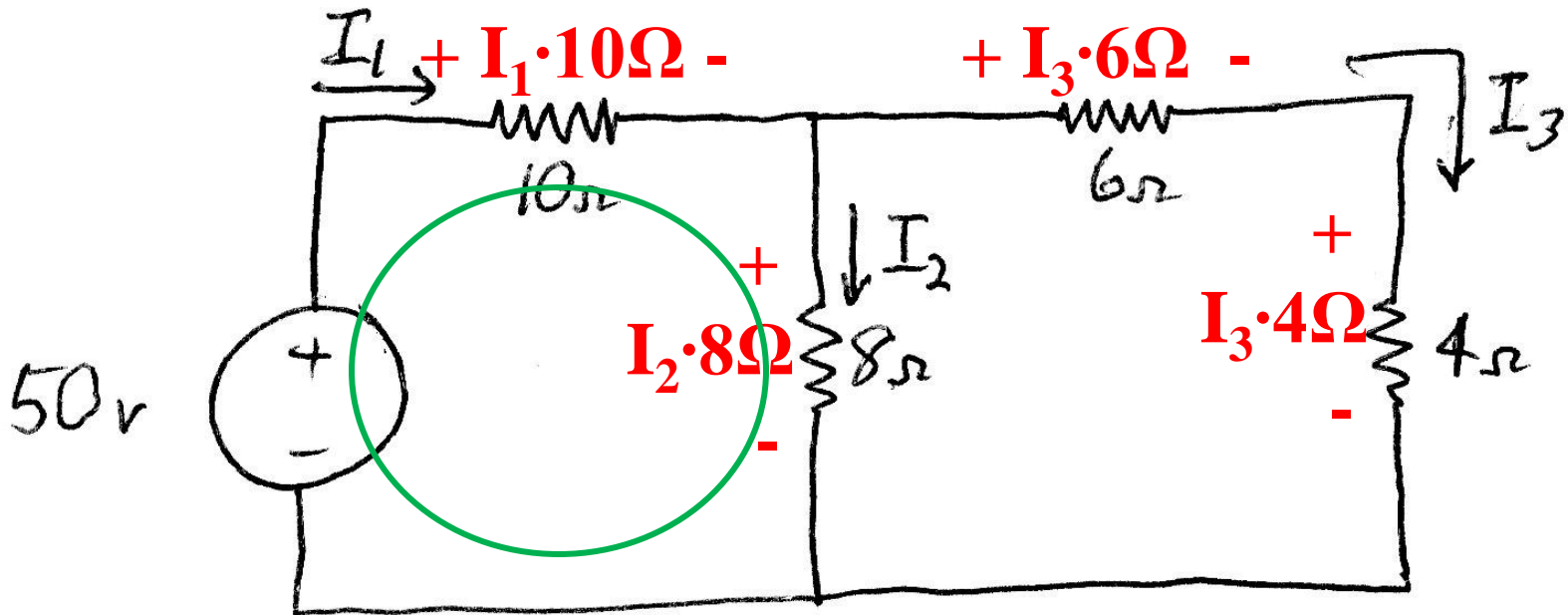
**Solve for the currents through each resistor
And the voltages across each resistor**

EXAMPLE CIRCUIT



Using Ohm's law, add polarities and expressions for each resistor voltage

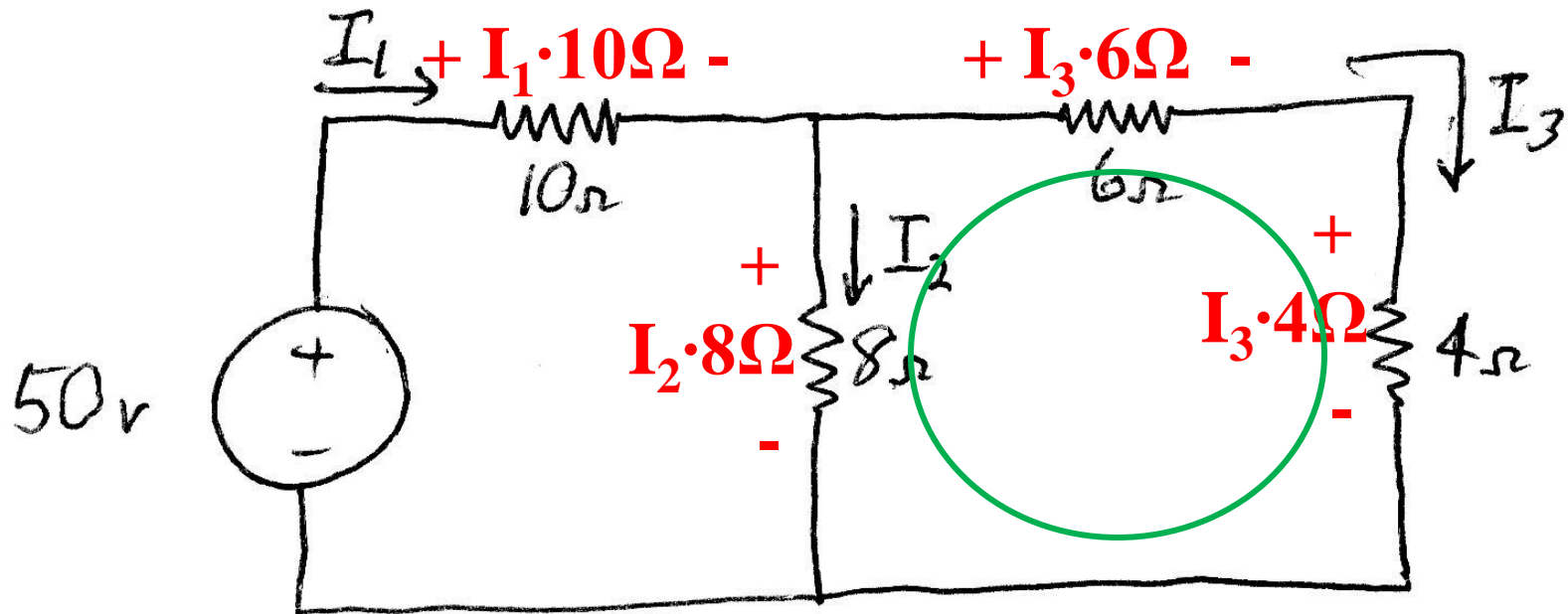
EXAMPLE CIRCUIT



Write 1st Kirchoff's voltage law equation

$$-50 \text{ v} + I_1 \cdot 10\Omega + I_2 \cdot 8\Omega = 0$$

EXAMPLE CIRCUIT

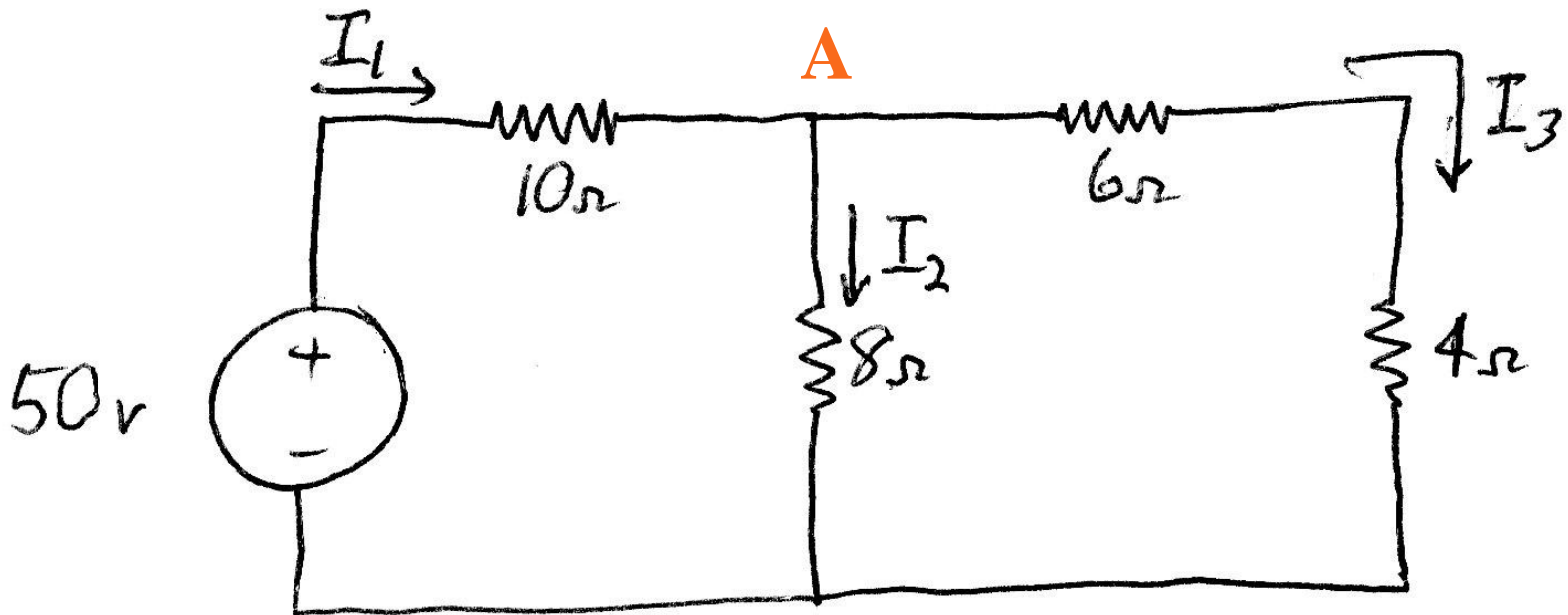


Write 2nd Kirchoff's voltage law equation

$$-I_2 \cdot 8\Omega + I_3 \cdot 6\Omega + I_3 \cdot 4\Omega = 0$$

$$\text{or } I_2 = I_3 \cdot (6+4)/8 = 1.25 \cdot I_3$$

EXAMPLE CIRCUIT



Write Kirchoff's current law equation at A
 $+I_1 - I_2 - I_3 = 0$

EXAMPLE CIRCUIT

We now have 3 equations in 3 unknowns, so we can solve for the currents through each resistor, that are used to find the voltage across each resistor

Since $I_1 - I_2 - I_3 = 0$, $I_1 = I_2 + I_3$

Substituting into the 1st KVL equation

$$-50 \text{ v} + (I_2 + I_3) \cdot 10\Omega + I_2 \cdot 8\Omega = 0$$

$$\text{or } I_2 \cdot 18 \Omega + I_3 \cdot 10 \Omega = 50 \text{ volts}$$

EXAMPLE CIRCUIT

But from the 2nd KVL equation, $I_2 = 1.25 \cdot I_3$

Substituting into 1st KVL equation:

$$(1.25 \cdot I_3) \cdot 18 \, \Omega + I_3 \cdot 10 \, \Omega = 50 \text{ volts}$$

$$\text{Or: } I_3 \cdot 22.5 \, \Omega + I_3 \cdot 10 \, \Omega = 50 \text{ volts}$$

$$\text{Or: } I_3 \cdot 32.5 \, \Omega = 50 \text{ volts}$$

$$\text{Or: } I_3 = 50 \text{ volts} / 32.5 \, \Omega$$

$$\text{Or: } I_3 = 1.538 \text{ amps}$$

EXAMPLE CIRCUIT

Since $I_3 = 1.538$ amps

$$I_2 = 1.25 \cdot I_3 = 1.923 \text{ amps}$$

Since $I_1 = I_2 + I_3$, $I_1 = 3.461$ amps

The voltages across the resistors:

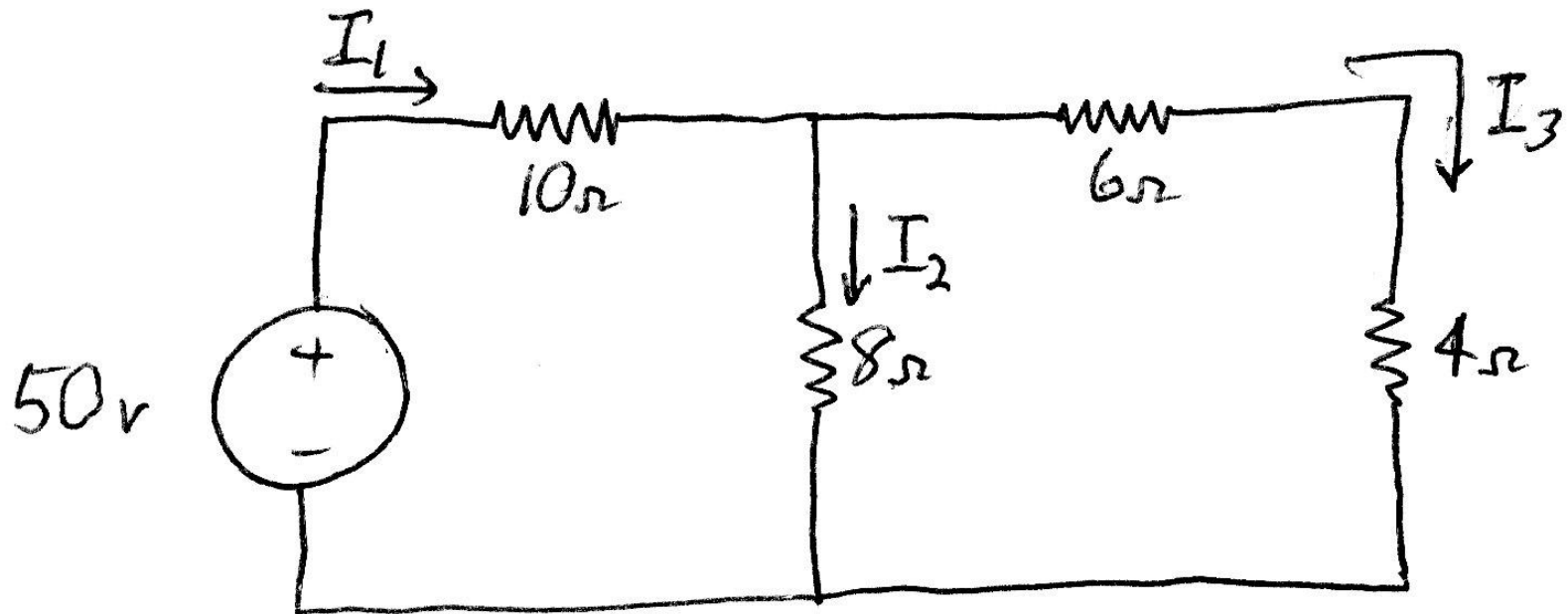
$$I_1 \cdot 10\Omega = 34.61 \text{ volts}$$

$$I_2 \cdot 8\Omega = 15.38 \text{ volts}$$

$$I_3 \cdot 6\Omega = 9.23 \text{ volts}$$

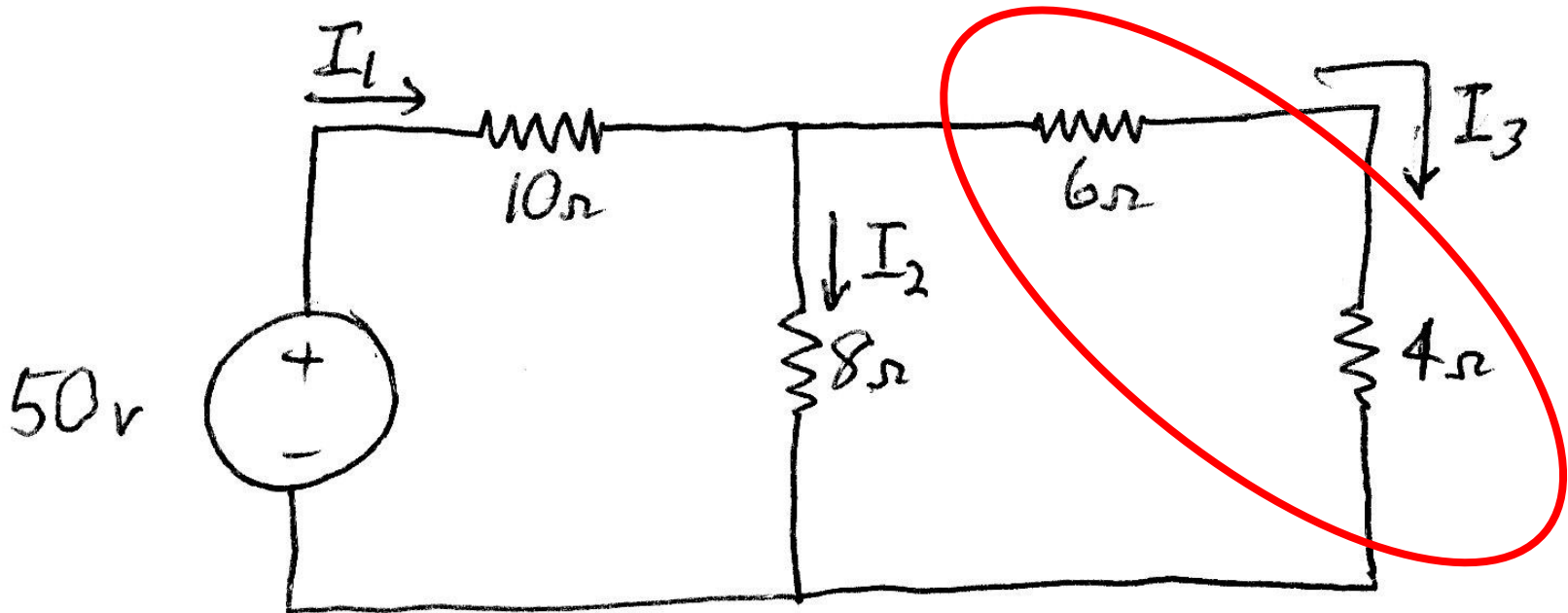
$$I_3 \cdot 4\Omega = 6.15 \text{ volts}$$

EXAMPLE CIRCUIT



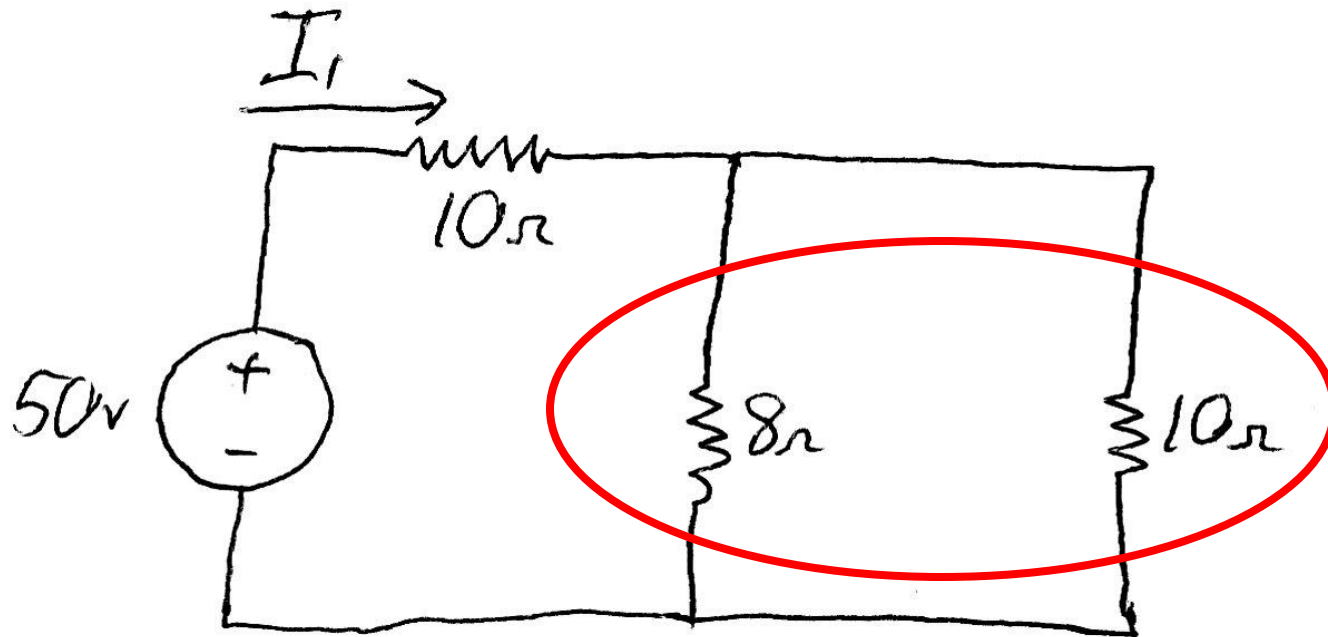
**Solve for the currents through each resistor
And the voltages across each resistor using
Series and parallel simplification.**

EXAMPLE CIRCUIT



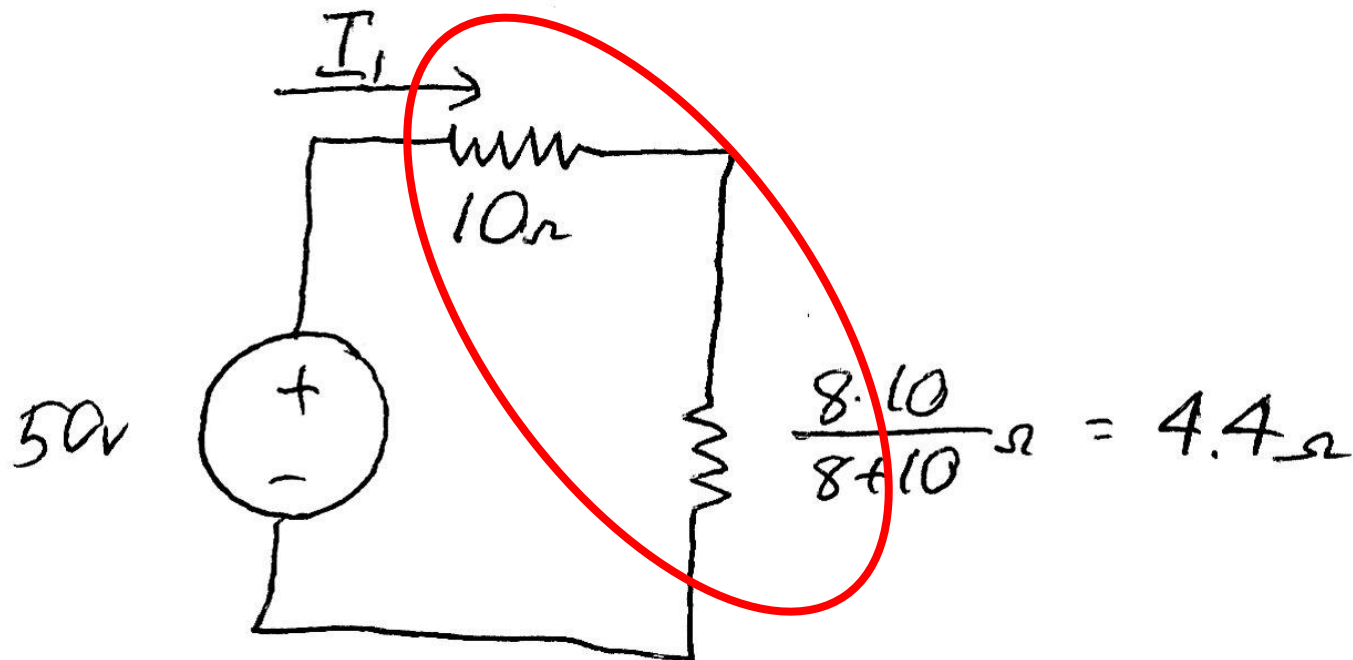
The 6 and 4 ohm resistors are in series, so are combined into $6+4 = 10\Omega$

EXAMPLE CIRCUIT



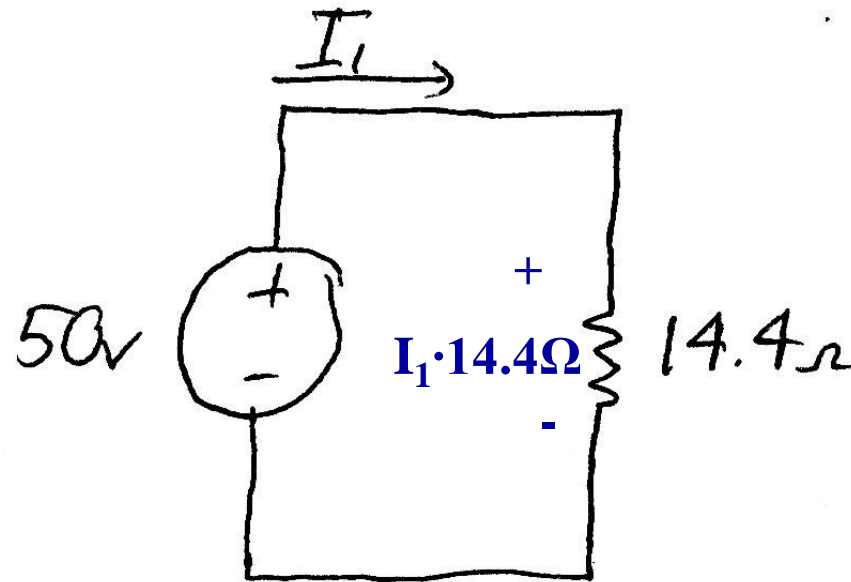
The 8 and 10 ohm resistors are in parallel, so are combined into $8 \cdot 10 / (8 + 10) = 14.4 \Omega$

EXAMPLE CIRCUIT



The 10 and 4.4 ohm resistors are in series, so are combined into $10+4 = 14.4\Omega$

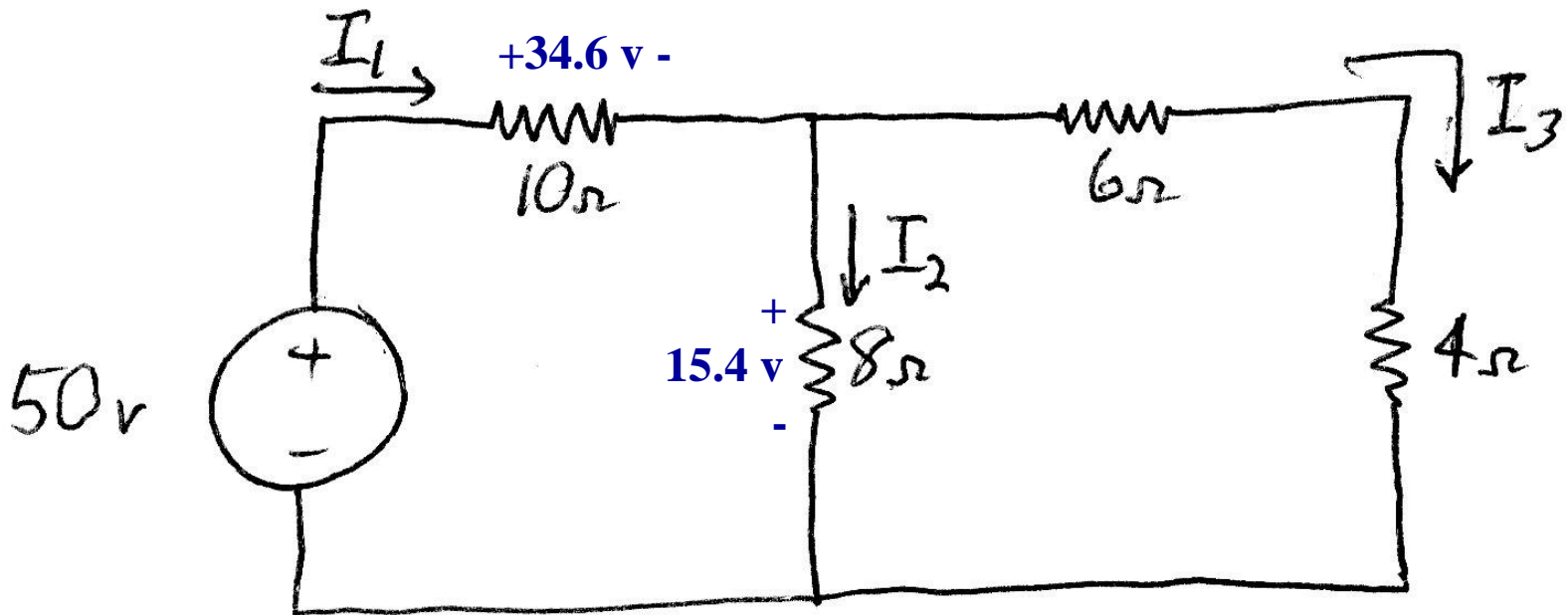
EXAMPLE CIRCUIT



Writing KVL, $I_1 \cdot 14.4\Omega - 50 \text{ v} = 0$

Or $I_1 = 50 \text{ v} / 14.4\Omega = 3.46 \text{ A}$

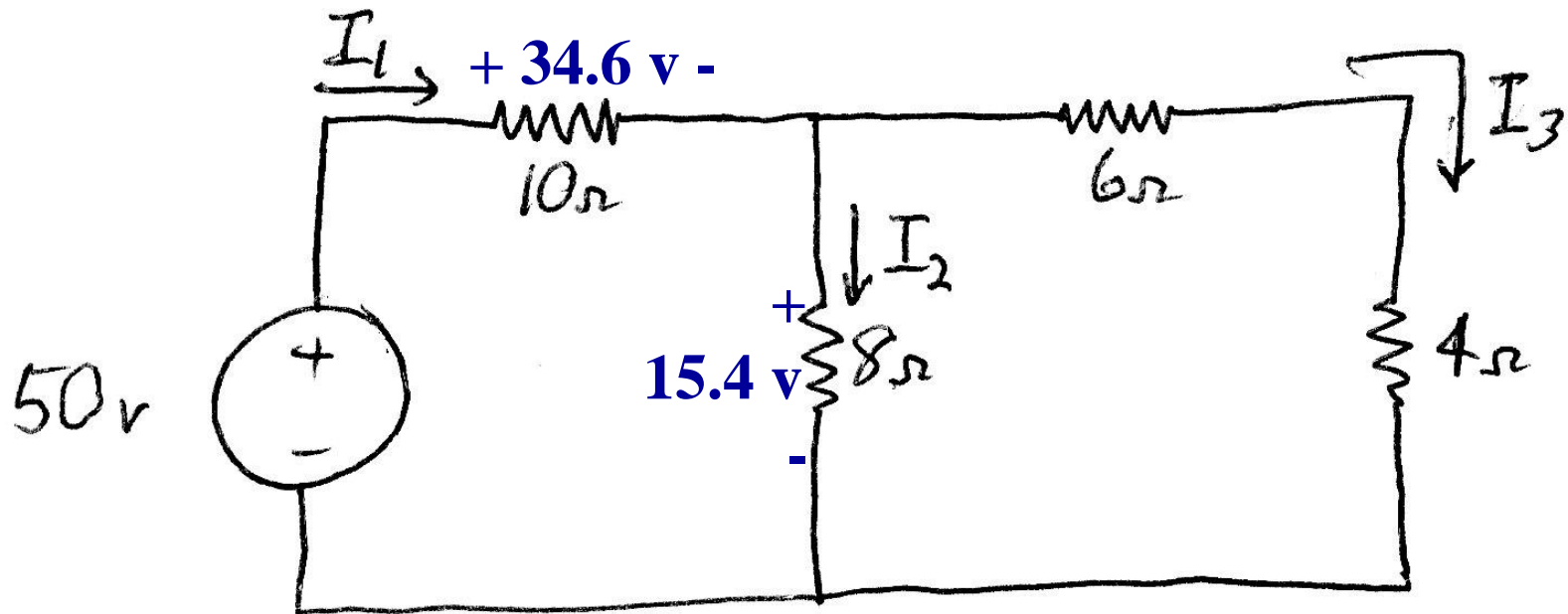
EXAMPLE CIRCUIT



If $I_1 = 3.46$ A, then $I_1 \cdot 10 \Omega = 34.6$ v

So the voltage across the $8 \Omega = 15.4$ v

EXAMPLE CIRCUIT



If $I_2 \cdot 8 \Omega = 15.4 \text{ v}$, then $I_2 = 15.4/8 = 1.93 \text{ A}$
By KCL, $I_1 - I_2 - I_3 = 0$, so $I_3 = I_1 - I_2 = 1.53 \text{ A}$

KIRCHHOFF'S VOLTAGE LAW (KVL)

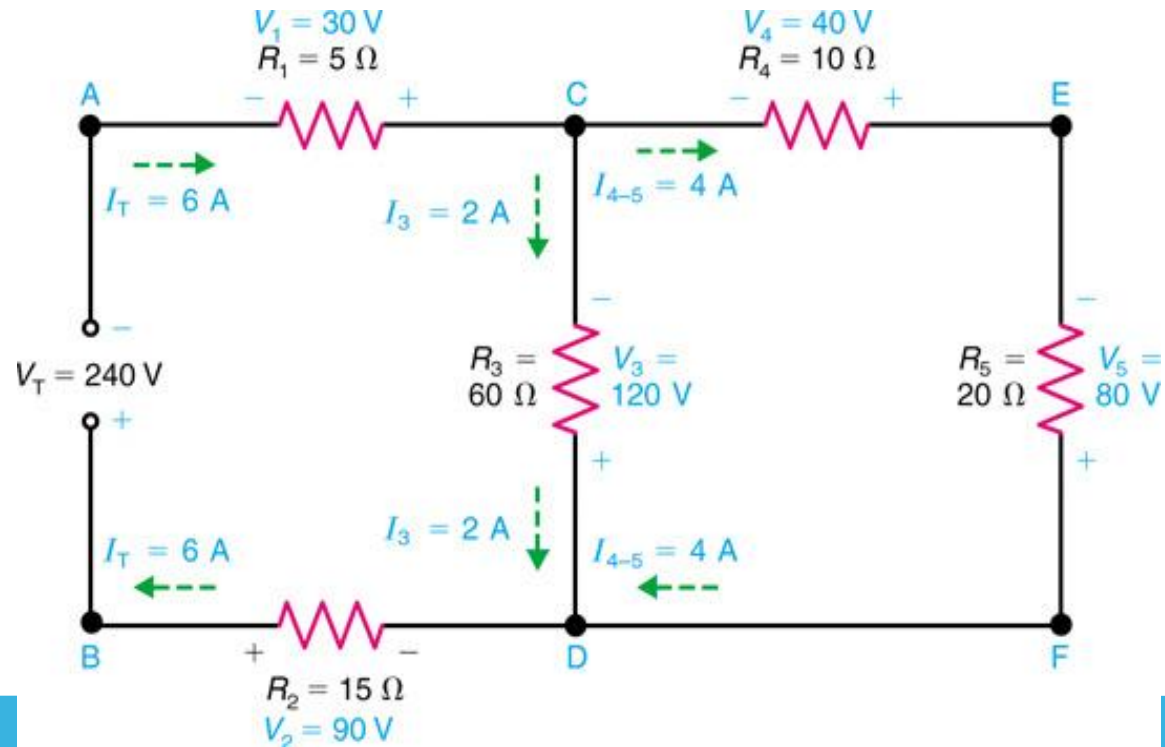


Fig. 9-2: Series-parallel circuit illustrating Kirchhoff's laws.

KIRCHHOFF'S VOLTAGE LAW (KVL)

In Figure 9-2, for the inside loop with the source V_T , going counterclockwise from point B,

$$90V + 120V + 30V = 240V$$

If 240V were on the left side of the equation, this term would have a negative sign.

The loop equations show that KVL is a practical statement that the sum of the voltage drops must equal the applied voltage.

KIRCHHOFF'S VOLTAGE LAW (KVL)

The algebraic sum of the voltage rises and IR voltage drops in any closed path must total zero.

For the loop **CEFDC** without source the equation is

$$-V_4 - V_5 + V_3 = 0$$

$$-40V - 80V + 120V = 0$$

$$0 = 0$$

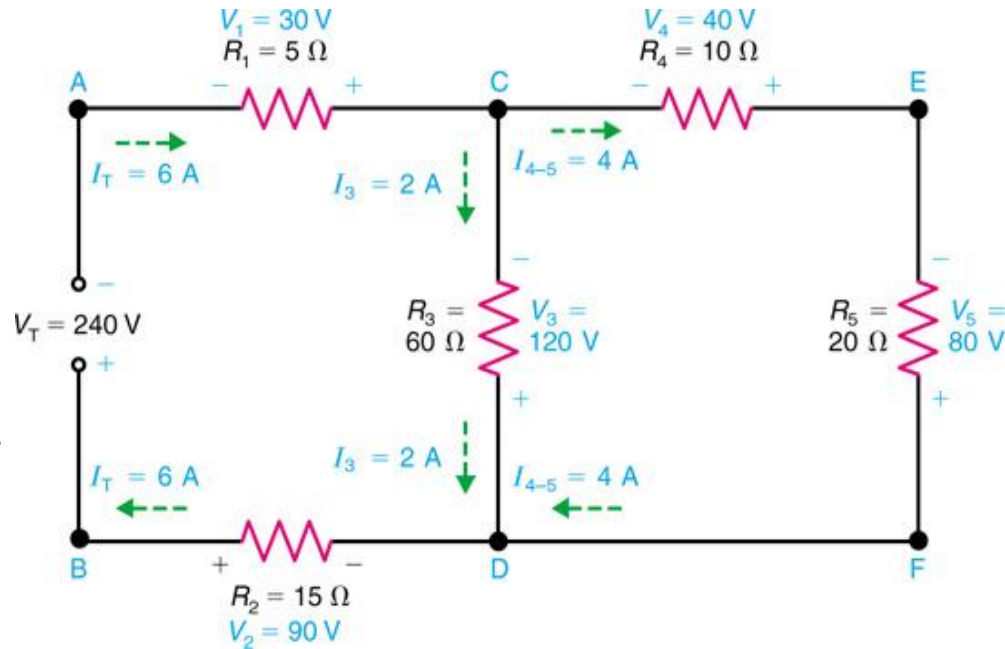


Fig. 9-2: Series-parallel circuit illustrating Kirchhoff's laws.